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PAGE	2	M	NIATHIA	III	j	MEPROT		
000440 000441 000442 000443		96 97 98 99 100 101 102 103 104 105		BSS BSS BSS BSS	E DATA TABLE  1 1 2 2 1 2 1 2 1 2 1 2 1 2 1 2 1 2 2 1 2 2 1 2 2 1 2	LOOP ON ERROR FLAG, 0=DON'T LOOP 1=LOOP MEMORY SIZE (HIGHEST AVAIL CORE) 0=CONSOLE MODE 1=TTY MODE DIGIT COUNTER FOR INPG	0 1 0 1 0 1 0 1 0 1 0 1	00096 00097 00098 000 001 00101 00102 00103 00104 00105
000500	000045 A	106 107 108 109 110	* ****** * MPM	SET ORG	045 050 <b>0</b>	**************************************	0 1 0 1	00106 00107 00108 00109
000500	002000 A 001410 A	111	TST1	CALL	LBBT	LOW BLOCK BOUNDARY TEST (SAVE AND SET LOC.)	0 1	00111
000502 000503 000504 000505 000506		112 113 114	TSTA	LDA STA CAL <b>L</b>	DNE ERRC ILAS, JPM, TSTZ	SET UP ERROR CODE ONE ** 1 **  SET INTERRUPT ADDRESS TO TSTZ	0 1 0 1 0 1	00112 00113 00114
000511	000520 A 002000 A 001201 A 001635 A	115		CALL	SETM, MSKO		0 1	00115
000513 000514 000515	100645 A 011641 A	116 117 118 119		EXC LDA STA JMP	*+2 *+2 0600+MPM	ENABLE MP  JUMP TO LOW BOUNDARY (1ST UNPROTECTED LOC)	0 1	00116 00117 00118 00119
000520 000521 000522 000523 000524 000525	000000 A 100745 A 102545 A 051100 A 011101 A	120 121 122 123 124 125	TSTZ	ENTR EXC CIA STA LDA JAN	0700+MPM MPM LOCP LPFE *+7	DISABLE MP	0 1 0 1 0 1 0 1	00120 00121 00122 00123 00124 00125
000527 000530	010440 A 001010 A 000534 A	126 127		L D A J A Z	\$FLG *+4			00126 00127
000532		128		CALL	ERRS		0 1	00128
000534	002000 A	129		CALL	RLBB	RESTORE LOW BLOCK BOUNDARY	0 1	00129
000536 000537	002000 A 001204 A	130		CALL	CK2E	CHECK SS3 AND HLTF FLAG.	0 1	001~~
	141641 A 005311 A	131 132 133 134		LDA SUB Dar Jaz	LDCP LLDC *+4	CHECK IF INST ADDRESS REGISTER CORRECT	0 1 0 1	00132
	000547 A	135		CALL	IARE	CHECK FOR ERROR		00134
000546 000547 000550	001036 A 011101 A 001010 A	136 137		LDA	LPFE *+4	CHECK FOR LOOPING NON ERROR	0 1	00136 00137
000552	기계 (숙마, 아래의 회원들을 제공하기)	138		CALL	IARE		0 1	00138
	001036 A 002000 A	139		CALL	CKSE	CHECK SS3 AND HLTF FLAG.	0 1	00139
000556	006010 A 000566 A	140		LDAI	TSTB		0 1	00140
000560 000561 000562	051645 A 006010 A 000605 A	141		STA LDAI	LOOP	SET UP LOOP ADDRESS		00141 00142
000565	051646 A 002000 A 001342 A 011066 A	143	TSTB	STA CALL LDA	CONT HBBT	SET UP CONT. ADDRESS HIGH BOUNDARY BIT TEST (SAVE AND SET LOC) SET UP ERROR CODE THO. ** 2 **	01	00143 00144 00145
000567 000570 000571 000572	051644 A 002000 A 001133 A	146	1012	STA	ÉRRC ILAS, JPM, TSTY	SET INTERRUPT ADDRESS TO TSTY	01	00146
000574 000575	18 16 16 17 17 18 18 18 18 18 18 18 18 18 18 18 18 18	148		CALL	SETM, MSKO		0 1	00148
000601	100645 A 011642 A 005311 A 050604 A	149 150 151 152		EXC LDA BAR STA	0600+MPM HLOC *+2	JUMP TO HIGH BOUNDARY-1 (LAST UNPROTECTED	0 1 0 1 0 1 0 1	00149 00150 00151 00152
000604 000605 000606 000607 000610	001000 A 000603 A 0000000 A 100745 A 010440 A 001010 A	154 155 156 157	TSTY	JMP ENTR EXC LDA JAZ	** 0700+MPM \$FLG *+4	DISABLE MP		00153 00154 00155 00156 00157
000612	[1] - [1] - (UDAUD (UDAUD) [2]) - (UDA	158		CALL	ERRS		0 1	0.0
000614	002000 A 001373 A	159		CALL	RHBB	RESTORE HIGH BLOCK BOUNDARY	0 1	001.9
000616	H. H	160		CALL	CKSE	CHECK SS3 AND HLTF FLAG.	0 1	00160

	M	AINTAIN	III	3	1EPROT	PAG	E 3
000620 006010 A	161		LDAI	TSTC		0 1	00161
000622 051645 A P 623 006010 A 1 324 000647 A	162 163		STA LDAI	LOOP	SET UP LOOP ADDRESS		00162
000625 051646 A 000626 002000 A 000627 001250 A	164 165		STA	CONT SSMB	SET UP CONT. ADDRESS SAVE AND SET 12 HORD IN MIDDLE OF BLOCK		00164 00165
000630 011067 A 000631 051644 A	166 167	TSTC	L D A S T A	THRE	SET UP ERROR CODE THREE. ** 3 **		00166
000632 002000 A 000633 001133 A 000634 000010 A	168		CALL	ILAS, JPM, ERRS	SET INTERRUPT ADDRESS TO ERRS		00168
000635 000746 A 000636 100745 A 000637 002000 A 000640 001201 A 000641 001635 A	169 170		CALL	0700+MPM SETM, MSKO	DISABLE MP SET BLOCK UNPROTECTED		00169 00170
000642 011641 A 000643 121075 A 000644 050646 A	171 172 173		LDA ADD STA	LLDC N371 *+2	CALC. TEST SUB.	0 1 0 1	00171 00172 00173
000645 002000 A 000646 000000 A 000647 000000 A	174	XTST	JMPM ENTR	0	JUMP TO SUB.	01	00174
000650 011070 A 000651 051644 A 000652 100745 A	176 177 178		LDA STA EXC	FBUR ERRC 0700+MPM	SHOULD RETURN WITH NO INTERRUPT ** 4 **  SET UP ERROR CODE FOUR.  DISABLE MP	0 1 0 1 0 1	00176 00177 00178
000653 002000 A 000654 001234 A 000655 006010 A	179		LDAI	TSTX	PROTECT ALL AREAS.	01	00179
000656 000647 A 000657 051645 A 000660 006010 A	181		STA LDAI	LOOP	SET UP LOOP ADDRESS		00181
000661 000670 A 000662 051646 A 000663 011641 A	183		STA	CONT	SET UP CONTINUE ADDRESS	0 1 0 1	00183
000664 121074 A	185 186		ADD	N374 *+2	CALC TEST SUB.	0 1 0 1	00185
000666 002000 A 000667 000000 A 000670 000000 A	187	TSTW	JMP <b>M</b> Ent <b>r</b>	0	JUMP TO SUB	01	00187
000671 011071 A 000672 051644 A 000673 002000 A 000674 001234 A	189 190 191		LDA STA CAL <b>L</b>	FIVE ERRC CLMP	SHOULD RETURN WITH NO ERRORS OR INTERRUPT.  SET UP ERROR CODE FIVE. ** 5 **  PROTECT ALL AREAS.	0 1	00189 00190 00191
000675 100645 A 0 76 006010 A	192 193		EXC LDAI	0600+MPM TSTU	ENABLE MP		00192
0	194 195		STA	LOOP	SET UP LOOP ADDRESS		00194 00195
000703 051646 A 000704 011641 A	196 197		STA LDA	CONT	SET UP CONTINUE ADDRESS.	0 1 0 1	00196
000705 121077 A 000706 050710 A 000707 002000 A	198 199 200		DDA ATS Mamu	N377 *+2	CALC TEST SUB.	01	00198
000710 000000 A 000711 000000 A		TSTV	ENTR	0	JUMP TO SUB.	01	00200
000712 011072 A 000713 051644 A 000714 100745 A	202 203 204		LDA STA EXC	SIX ERRC 070 <b>0+MPM</b>	SHOULD RETURN WITH NO ERRORS OR INTERRUPT.  ERROR CODE ** 6 **  DISABLE MP	0 1 0 1 0 1	00202 00203 00204
000715 002000 A 000716 001201 A 000717 001635 A	205		CALL	SETM, MSKO	UNPROTECT BLOCK	0 1	00205
000720 006010 A 000721 000711 A	206		LDAI	VIST		0 1	00206
000722 051645 A 000723 006010 A 000724 000740 A	207 208		STA LDAI	LDDP	SET UP LOOP ADDRESS		00207 00208
000725 051646 A 000726 0020 <b>0</b> 0 A 000 <b>7</b> 27 001133 A 000 <b>7</b> 30 000010 A	209		STA	CONT ILAS, JPM, TSTU	SET UP CONTINUE ADDRESS SET UP INTERRUPT ADDRESS		00209
000731 000740 A 000732 100645 A 000733 011641 A	211		EXC LDA	0600+MPM LLDC	ENABLE MP		00211
000734 121073 A 000735 050737 A	213 214		ADD Sta	N402 *+2		50.00	00213
000736 002000 A 000737 000000 A 000740 000000 A	215	TSTII	JMPM	0			00215
000741 100545 A 000742 002000 A	217	TSTU	ENTR EXC CALL	050 <b>0+MPM</b> RMBL	DISABLE MP. RESTORE MIDDLE BLOCK LOCATIONS.	0 1	00216 00217 00218
000743 001322 A 000744 001000 A 000745 100772 A	219		JMP*	TSTL		0 1	00519
STOLIS TOULLE H	221	*	***************************************		*********************************	<b>*</b> 01	00220
	222	*		R SUBROUTINE	**************************	0 1 0 1	00223
000746 000000 A 000747 100745 A 000750 011645 A 000751 050767 A		ERRS	ENTR EXC LDA STA	0700+MPM LOOP ERR1+5	DISABLE MP	0 1 0 1 0 1	00224 00225 00226 00227 00228

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PAGE	4	M	AINTAIN	III		MEPROT		
000752 011646 000753 005111 000754 050766 000755 050771 000756 011643 000757 021644 000760 060764 000761 030746		230 231 233 233 233 235 236		LDA IAR STA STA LDA LDB STB LDX	CONT  ERR1+4  ERR2+1  MASK  ERRC  ERRC  ERR1+2  ERRS	TERMINATION ABDRESS CONTINUE ADDRESS	0 1 0 1 0 1 0 1 0 1 0 1	00229 00230 00231 00' 00234 00235 00236
000763	0000000 A 101553 A	237	ERRI	CALL	SSUT, OO, (ERMG	>*,0,0	0 1	00237
000767 000770 000771		1 = 50	ERR2	JMP	0		0 1	00538
		239 240 241 242	* * *	MASK	REGISTER TEST		*01 *01 *01	00239 00240 00241 00242
000773	0000000 A	243 244 245	TSTL	ENTR LDAI	. 8	**************************************	01	00243 00244 00245
000775 000776	000010 A 051644 A 100745 A 002000 A	246 247 248		STA EXC CALL	ERRC 0700+MPM CLMP	DISABLE MP SET ALL MASKS TO 1 (ALL PROTECTED )	0 1 0 1 0 1	00246 00247 00248
001001	005201 A 051635 A 051636 A	249 250 251 252		COMP STA STA STA	1 MSK0 MSK0+1 MSK0+2		0 1 0 1 0 1 0 1	00249 00250 00251 00252
001005 001006 001007 001010	051640 A 005004 A 011643 A 141064 A	253 254 255 256		STA TZX LDA SUB	MSK0+3 MASK SIXT	SETUP MASK TABLE NITH SPECIFIED BIT ZERO.	0 1 0 1 0 1 0 1	00253 00254 00255 00256
001012	001016 A 005144 A	257		JAN IXR	*+5 *-4		01	00257
001015	001000 A 001010 A 121064 A 121076 A	259 260 261		JMP ADD ADD	SIXT N424	BUILD A ROTATE LEFT INST.	0 1 0 1	00259 00260 00261
001020	051022 A 011065 A	262 263 264		STA LDA NDP	#+5	BUILD A MASK WITH BIT TO BE CHANGED	0 1	00262 00263 00274
001023	005211 A 006055 A	265		CPA STAE	MSK0,1	BUILD H MASK WITH BIT TO BE CHANGED	0 1 0 1 0 1	00.
001026	006010 A 000502 A	267		LDAI	TSTA	SET UP LOOP ADDRESS		00267
001030 001031 001032	006010 A	268 269		STA LDA <b>I</b>	TSTZ	SET UP CONTINUE ADDRESS	01	00268
001033	051646 A 001000 A	270 271		STA	CONT TST1			00270 00271
001036 001037	000000 A 006010 A	272 273	IARE	ENT <b>R</b> LDAI	7	INST. ADDRESS REGISTER ERROR		00272 00273
001042	051644 A 006010 A	274 275		STA LDAI	ERRC		0 1 0 1	00274 00275
001044	001052 A 051645 A 006010 A	276 277		STA LDAI	LOOP			00276 00277
001046 001047 001050		278 279		STA	CONT ERRS		0 1 0 1	
001054	005201 A 051101 A 001000 A	280 281 282		COMP STA JMP	1 LPFE TST1	LOOPING INST. ADDRESS REG. LOOP FLAG	0 1 0 1 0 1	00280 00281 00282
	051101 A	283 284 285 286		ENTR TZA STA JMP*	LPFE		0 1 0 1 0 1 0 1	00283 00284 00285 00286
001063 001065 001065 001067 001070 001071 001073 001073 001075 001077 001101 001101	000007 A 0000020 A 000001 A 000002 A 000003 A	289012399 299399 29939 2993 2993 2993 2993 2	DNE THRE THRE FIX N374 N374 N374 N374 N100	DATA DATA DATA DATA DATA DATA DATA DATA	7 16 1 2 3 4 5 6 0402 0374 0371 04240 0377 0 0100 0177677		0 1 0 1 0 1 0 1	00287

			MA	NIATNI	III		MEPROT	PAG	E 5
001104	000000	A					D ************************************	<b>*</b> 01	00304 00305
			306 307 308			SEQUENCE		<b>*</b> 01	00306 00307 00308
			309 310	* *	THIS SUI	L ILAS (BIT, PR BROUTINE STORE CATION DEFINED	OCESSOR LOC) S THE LOCATION SPECIFIED BY 2ND TERM IN BY BIT VALUE. IF BIT IS ONE, ADDRESS	*01 *01 *01	00309 00310 00311 00312
	000001	A	314	******* HALT OVER	EQU EQU	*********** 01 020	**************************************	0 1	00313 00314 00315
	000002	A	316 317	I DE WRT	EQU EQU	0 2 0 4	I/O ERROR 0122 WRITE 0124	0 1 0 1	00316 00317
	$000040 \\ 000100$	A	318 319 320	JPM IDED WRTD	EQU EQU	010 040 0100	JUMP 0126 I/O ERROR/OVERFLOW 0132 WRITE/OVERFLOW 0134	0 1 0 1	00318 00319 00320
	000200 031133 015000	A A	321 322 323	JPM0 ILA1	EQU LDX LDA	0200 ILAS 0,1	JUMP/OVERFLOW 0136 GET BIT	0 1	00321 00323
001107 001110 001111	025000 006030	A A	324 325 326		IXR LDB LDXI	0 · 1 16	GET PROC. LOC.	0 1	00324 00325 00326
001113	000020 004250 001002	A A A	327 328	ILA2	LRLA JAP	*+3	CHECK IF BIT SET		00327 00328
001116	T. T	A	329 330		STB JXZ	017,1 ILA3	STORE PROC. LOCATION		00330
001120	001126 005344 005344	A A	331 332		DXR DXR		DECREMENT INTERRUPT LOC. POINTER		00331
001123 001124	004241 001000 001114	9 9	333 334		LRLA JMP	1 ILA2	SHIFT A RIGHT.	0 1	00333 00334
001126 001127	002000	A	1202112	ILA3	CALL	RESR	RESTORE REGISTERS		00335
001130 001131 001132 001133	041133	9 9 9	336 337 338		INR INR JMP	ILAS ILAS 0		0 1	00336 00337 00338
001133	002000	В	339 340	ILAS	BES	0 SAV <b>R</b>	SAVE REGISTERS		00339 00340
001136	001144 001000 001105	A	341		JMP	ILAI			00341
8 8			The state of the state of	*			ATTACHED SECTION AND THE SECTION OF	<b>*</b> 01	00342 00343 00344
001140	051156	^	345 346	* *****	*****	******		*01 *01	00345 00346
001141	051156 061157 071160 001000		348 349 350	SAVI	STA STB STX JMP	SAVA+1 SAVA+2 0		0 1 0 1	00347 00348 00349 00350
$001144 \\ 001144$	000000	130		SAVR	BES JMP	0 SAV1		0 1	00351
	001140		353		STOCK FOR	V (500 100 100 100 100 100 100 100 100 100	***************	<b>*</b> 01	00353
			354 355 356	* *			ES VOLATILE REGISTERS	*01 *01	
	011156 021157		A 1770 ( 5 170 ( 5 170 )		******** LDA LDB	*********** SAVA SAVA+1	*****************		00357 00358 00359
001151 001152 001153	031160	A	360 361		LDX JMP	SAVA+2 0		0 1	00360
001153 $001154$	000000 001000 001147	A	362 363	RESR	BES JMP	0 RES1			00362 00363
001156		9.72	365		B22	3 ************	***************	55.77TV 77	00364
			366 367 368	* *	H ANDARASTIN N	DESCRIPTION OF THE PROPERTY OF	FROM TABLE DEFINED BY CALLING SEQUENCE.	*01 *01 *01	00366 00367 00368
001161	006037	A	369 370	******* SET1	LDXE*	SETM *******	*****************	*01 01	00369 00370
001164	041201 100045 015000	A	371 372 373		INR EXC LDA	SETM MPM 0,1	SELECT MASK REGISTER 0	0 1 0 1 0 1	00371 00372 00373
001166	103145	AAA	374 375		DAR EXC	MPM 0100+MPM	DUTPUT TO MASK REGISTER 0 Select mask register 1	0 1 0 1	00374 00375
001171	015001 103145 100245	A	376 377 378		LDA OAR EXC	1,1 MPM 0200+MPM	OUTPUT TO MASK REGISTER 1 Select mask reg. 2	0 1	00376 00377 00378
01 74	015002 103145 100345	A	379 380 381		LBA BAR EXC	2,1 MPM 0300+MPM	DUTPUT TO MASK REG. 2 SELECT MASK REG. 3	0 1 0 1	00379 00380 00381
001176 001177	015003 103145 001000	A	382 383 384		LDA DAR JMP	3,1 MPM	DUTPUT TO MASK REG. 3	0 1 0 1	00382
[ - 12 M N HOND TO THE STREET OF THE STREET	000000		504		Jane	•	RETURN TO CALLING PROGRAM.	0.1	00384

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PAGE	6		MA	NIATAIN	III	I=	MEPROT		
	001000	A	385 386	SETM	JMP	0 SET1			00385
001203	001161		387 388		******	*********	*************	₩01 ₩01	00′
			389	*			SSS AND EXECUTIVE EXIT FLAG.	*01 *01	00389
	000000	A		CKSE	ENTR LDA	**************************************	************************	0 1 0 1 0 1	00391 00392 00393
001206	$001010 \\ 001220$	A	394		JAZ	*+10			
001210 001211 001212		A	395 396 397		TZA STA LDA	HLTF LDCZ	•	0 1 0 1 0 1	00395 00396 00397
001213	050000	Á	398 399		STA LDA	0 LOCZ+1		0 1 0 1	00398
001216	$050001 \\ 001000 \\ 000000$	A	400 401		AT2 JMP	0 1		01	$00400 \\ 00401$
001220	001400 003136	A	402		7223	MPT1			00402
001222 001223 001224	101204	A	403 404	LOCZ	JMP# DATA	0,0 CKSE	LOCATION ZERO AND DNE SAVED.		00403 00404
001225	00000		405					<b>*</b> 01	00405
001226	041233	A	406 407 408		INR	DLE INTERRUPT	PRUCESSUR	*01 *01	00406 00407 00408
001227 001230 001230	001000	A	409	HLTR	JMP JMP	0			00409
001231	001000	A	411	**************************************	JMP	<b>*</b> -3		AND AND ADDRESS OF THE PARTY OF	00410
001233	000000		412 413 414	HLTF ******	DATA *******	O ************************************	**************************************		00412 00413 00414
001234	하다 얼마나 하고 있는 아이를 살아 있다.	A	415 416		ENTR	*******	*****************************		00415
001235 001236 001237	005201 100045 103145	A	417 418 419		COMP EXC OAR	MPM MPM	MASK 0	0 1 0 1 0 1	00417 00418 00419
001240	100145	A	420 421		EXC DAR	0100+MPM MPM	MASK 1	0 1 0 1	00420
001242 001243 001244	103145	A	422 423 424		EXC DAR EXC	0200+MPM MPM 0300+MPM	MASK 3	0 1 0 1 0 1	004-4
	() 보이스 프라이어 () () () () () () () () () () () () ()	A	425 426		OAR JMP*	MPM CLMP	RETURN	0 1	() 기계 () 1 () 1 () () () () () () () () () () () () ()
001247	101254		427 428	*			*******************************	*01	00428
			429 430 431	*	BLOCK SI		RDS IN MIDDLE OF BLOCK.	*01 *01	
	•	à	432 433	*	JMPM* Entr	<b>*</b> − 1		*01 *01	00432
			434 435 436	*	JMPM* ENTR JMPM*	*-1 *-1		*01 *01	어린 전에 작년을 가 먹니 아이들이 없었다.
				*	ENTR JMPM	ERRS		*01 *01	00437 00438
001250		A	440 441	******	ENTR		*************	* 0 1	00439 00440 00441
001251 001252 001253	006120		442 443		L D A A D D I	0371	SAVE 12 HORDS STARTING AT LLOC + 0371	01	00442
001254 001255 001256	006030		444 445		TAB LDXI	1 1			00444
001257	016000 006055	A	446 447	SSM1	L DA Stae	0,2 SVLB,1			00446 00447
001261 001262 001263	001040	AAA	448		JXZ	SSMS		0 1	00448
001264 001265	005344 005122	A	449 450 451		DXR IBR JMP	SSM1		-52.5	00449 00450 00451
001267 001270 001271	얼마나 얼마나 어린다 얼마나 하다.	A		SSWS	L D A A D D I	LLOC 0371	SET UP JUMP INST	0 1	00452 00453
001272 001273 001274	005012 006010	À	454 455		TAB LDAI	02000	JUMP AND MARK INST	n 1	00454
001276	002000 056001 056004		456 457		STA	1.2		0 1 0 1	00456
$001300 \\ 001301$	056007 056012	A A	458 459 460		STA	7,2 10,2	•	0 1	00 004 <del>5</del> 9
001303 001304	000746 056013	A A	461 462		STA TBA	11,2	SETUP JUMP INDIRECT BACK TO CALLING PROG.	0 1	00461
		323			1502m2513/6		OLIVE COME THE PROPERTY OF CHECKING PROGRE	0.1	90406

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001306 111647 A 001307 056002 A 001310 005111 A C 311 005111 A U 312 005111 A 001313 056005 A	463 464 465 466 467 468	ORA STA IAR IAR IAR STA	BT15 2,2 5,2		0 1	00463 00464 00465 00466 00467 00468
001314 005111 A 001315 005111 A 001316 005111 A 001317 056010 A 001320 001000 A 001321 101250 A	469 470 471 472 473	IAR IAR IAR STA JMP#	8,2 8,2	RETURN TO CALLING PROGRAM.	0 1 0 1 0 1	00469 00470 00471 00472 00473
	475 × 476 ×			WORDS FROM MIDDLE OF BLOCK	*01 *01 *01	00474 00475 00476
001322 000000 A	477 * 478 ***** 479 RMBL	****** ENTR	*********	******************		00477 00478 00479
001323 011641 A 001324 006120 A 001325 000371 A	480 481	LDA ADDI	0371	RESTORE 12 WORDS	0 1	00480 00481
001326 005012 A 001327 006030 A 001330 000013 A	482 483	TAB LDXI	1 1			00482 00483
001331 006015 A 001332 001537 A 001333 056000 A	484 RMB1 485	LDAE	SVLB.1			00484
001334 001040 A 001335 101322 A	486	JXZ*	RMBL		0 1	00485
001336 005344 A 001337 005122 A 001340 001000 A 001341 001331 A	487 488 489	DXR IBR JMP	RMB1		0 1	00487 <b>004</b> 88 00489
	491 * 492 *	HIGH	BLOCK BOUNDARY	**************************************	*01 *01	00491 00492
001342 000000 A	495 HBBT	ENTR		**************************	*****01 01	00495
001343 021642 A 001344 005322 A 001345 016000 A	496 497 498	DBR LDA	0.5		0 1 0 1	00496 00497 00498
001346 051537 A 001347 006010 A 001350 001000 A	499 500	LDAI	01000 01000	JUMP INST.		00499 00500
001354 056000 A 0 52 016001 A 0 353 051540 A 001354 011642 A 001355 005111 A	501 502 503 504 505	STA LDA STA LDA IAR	0,2 SVLB+1 HLOC	LOC. OF PROTECTED CORE	0 1 0 1 0 1 0 1	00501 00502 00503 00504 00505
001356 056001 A 001357 016002 A 001360 051541 A 001361 006010 A 001362 002000 A	506 507 508 509	STA LDA STA LDAI	05000 215 218+5	JUMP AND MARK INST	0 1 0 1 0 1	00506 00507 00508 00509
001363 056002 A 001364 016003 A 001365 051542 A 001366 006010 A 001367 000746 A	510 511 512 513	STA LDA STA LDAI	2,2 3,2 SVLB+3 ERRS	ERROR SUBROUTINE ADDR.	0 1 0 1	00510 00511 00512 00513
001370 056003 A 001371 001000 A 001372 101342 A	514 515	STA JMP*	BBT .	RETURN TO CALLING PROG.		00514 00515
	517 ×			NDARY TEST LOCATIONS.	0 1 0 1 0 1	00516 00517 00518
001373 0000000 A 001374 021642 A 001375 005322 A 001376 011537 A 001377 056000 A 001400 011540 A 001401 056001 A 001402 011541 A 001403 056002 A 001404 011542 A 001405 056003 A 001406 001000 A 001407 101373 A	519 * 520 ***** 521 RHBB 522 523 524 525 526 527 528 529 531 532	******* ENTR LDB LDB LDA STA LDA STA LDA STA LDA STA LDA STA	**************************************	***************************************	0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1	00519 00521 00521 00523 00523 00523 00529 00531 00531
	534 * 535 *	LOW	BLOCK BOUNDARY	**************************************	*01 *01	00533 00534 00535 00536
001410 0000000 A 001411 021641 A 00 12 005322 A 0( 13 005322 A 00 +14 016000 A	537 ***** 538 LBBT 539 540 541	ENTR LDB DBR DBR	LLOC	***************************************	*****01 01 01 01 01	00537 00538 00539 00540 00541
00 -+14 016000 A 001415 051537 A 001416 006010 A 001417 002000 A 001420 056000 A	542 543 544 545	LDA STA LDAI STA	0,2 SYLB 02000 0,2	JMPM INST.	0 1 0 1	00542 00543 00544 00545

PAGE 8	MAINTAIN I	11	MEPROT	
001421 016001 A 001422 051540 A 001423 006010 A 001424 000746 A	547 S	DA 1,2 STA SVLB+1 DAI ERRS	0	1 00546 1 00547 1 00549
001425 056001 A 001426 016002 A 001427 051541 A 001430 006010 A	550 L 551 S	1,2 DA 2,2 STA SVLB+2 DAI 01000	JMP INST.	1 00551
001432 056002 A 001433 016003 A 001434 051542 A 001435 066003 A 001436 001000 A	554 L 555 S 556 S	2,2 DA 3,2 STA SVLB+3 STB 3,2 JMP# LBBT	LOC. OF LLOC-2	1 00553 1 00554 1 00555 1 00556 1 00557
001437 101410 A	559 ×		**************************************	1 00559
	560 * 561 * 562 *****		*0 *0 *0 *0 *0 ***********************	1 00561
001440 0000000 A 001441 021641 A 001442 005322 A 001443 005322 A 001444 011537 A 001445 056000 A 001446 011540 A 001447 056001 A 001451 056002 A 001452 011542 A 001453 056003 A 001454 001000 A	563 RLBB E 564 L 565 D 566 D 567 L 568 S 570 S 571 S 572 S 573 L	NTR DB LLOC  DBR DBR DA SVLB  STA 0,2  STA 1,2  STA 2,2  STA 2,2  STA 3,2  STA 3,2  SMP** RLBB		1 00563 1 00564 1 00565 1 00566 1 00567 1 00568 1 00569 1 00570
001455 101440 A 001456 146705 A	576 * 577 MSG1 I	MESSAGE BUFFERS	<b>*</b> 0	1 00576 1 00577
001457 146717 A 001460 151331 A 001461 120320 A 001462 151317 A 001463 152305 A 001464 141724 A 001465 120324 A 001465 120324 A 001467 152240 A 001470 106612 A 001471 000000 A 001472 141731 A 001473 141714 A 001473 141714 A 001474 142723 A 001477 146701 A		DATA CYCLES = ', O		1 00579
001500 151713 A 001501 120322 A 001502 142707 A 001503 127240 A 001504 152305 A 001506 106612 A 001507 000000 A 001510 151724 A 001511 140722 A 001512 152240 A 001513 152305 A 001514 151724 A 001513 152305 A 001514 151724 A 001515 120240 A 001516 130256 A 001517 136715 A 001520 140723 A 001521 145640 A 001522 151305 A 001523 143656 A 001523 143656 A 001524 120317 A 001525 151240 A 001526 130656 A	580 MSG4 I	DATA 'START TEST	0.=MASK REG. DR 1.=INST. TEST ',0106612,0 0	1 00580
001526 130655 A 001527 136711 A 001530 147323 A 001531 152256 A 001532 120324 A 001533 142723 A 001534 152240 A 001535 106612 A 001537 000000 A 001537 0011644 A 001554 011644 A 001555 006120 A 001556 120260 A 001557 051630 A 001561 005002 A 001562 006140 A 001563 000012 A 001564 001004 A	582 ERMG E 583 L 584 A 585 S 586 L 587 T 588 ERM1 S	BSS 12 ENTR DA ERRC ADDI 0120260 STA EMSG+13 DA MASK TZB SUBI 10 JAN ERM2	SET UP ERROR CODE IN ASCII	1 00581 1 00582 1 00583 1 00584 1 00586 1 00587 1 00588

	MAINTAIN	III		MEPROT	PAG	E 9
001565 001571 A 001566 005122 A 001567 001000 A 001567 0010562 A 001572 000012 A 001573 006120 A 001574 000260 A 001575 051603 A 001576 005021 A 001577 006120 A 001576 005021 A 001602 006120 A 001603 000000 A 001604 051623 A 001604 051623 A 001605 006030 A 001607 002000 A 001607 002000 A 001611 001000 A 001612 101553 A 001613 142722 A 001614 151317 A 001615 151240 A 001615 151240 A 001616 120302 A 001617 146317 A 001621 120275 A 001622 120275 A 001623 000000 A 001624 120275 A 001625 152331 A 001627 120275 A 001627 120275 A 001628 120275 A 001629 141713 A 001621 120275 A 001621 120275 A	MAINTAIN 590 591 592 ERM2 593 594 595 596 597 598 ERM3 599 600 601 602 603 EMSG	IBR JMP ADDI ADDI STA ADDI STA LDXI CALL* JMP* DATA	ERM1 10 0260 ERM3+1 0260 8 0 EMSG+8 EMSG UTD ERMG 'ERROR BLOCK	TEMP. STURAGE	01 01 01 01 01 01 01	00590 00591 00593 00593 00596 00598 00598 00601 00602 00603
001633 000000 A	606 * 607 * 608 CYCL	DATA	0	WRITE NON ERROR MESSAGE SUPPRESSION  0 = SUPPRESS  1 = WRITE MESSAGE  NO OF TIMES TO RUN TEST	<b>₩</b> 01	00605 00606 00607 00608
0' 35 177777 A 0 36 177777 A 00.637 177777 A 001640 177777 A 001641 0000000 A 001642 0000000 A 001643 0000000 A 001644 0000000 A 001645 0000000 A 001647 1000000 A	609 * 610 * 611 MSK0 612 MSK1 613 MSK2 614 MSK3 614 MSK3 615 LLOC 617 MASK 618 ERRC 619 LOOP 620 CONT 621 BT15	DATA DATA DATA DATA DATA DATA DATA DATA	0177777 0177777 0177777 0177777 0 0 0 0	1 TO X DEFINES NUMBER OF TIMES TO RUN.  CURRENT LOW BOUNDARY LOCATION CURRENT HIGH BOUNDARY LOCATION.  MASK BIT	*01 01 01 01 01 01 01 01	00619 00619 00613 00613 00613 00613 00617 00617 00619 00623
	624 <b>*</b> 625 <b>*</b>			IPT ADDRESS TEST UNPROTECTED CORE AREA.	*01 *01	00624 00625
002000 000000 A 002001 000001 A 002002 001000 A	628 TX01 629	****** RTM3 TLH THC *9ML	**************************************	**************************************	0 1 0 1 0 1	00626 00627 00628 00629 00630
002003 102000 A 002004 000000 A 002005 005001 A 002006 052777 A 002007 001000 A 002010 002777 A	631 TX02 632 633 634	ENTR TZA STA JMP	0 2777 0 2777	TEST THO STORE HALT INSTRUCTION IN LAST UNPROT. JUMP TO HALT INST.	0 1 0 1	00631 00632 00633 00634
002011 000000 A 002012 003000 A 002013 000002 A	635 TX03 636	EHTR XEC	0 2	TEST THREE Execute a xec command to execute a halt		00635 00636
002014 001000 A 002015 102011 A 002016 000000 A	637 638 TX04	JMP* ENTR	TX03	TEST FOUR	0 1	00637
002017 006010 A 002020 005122 A 002021 052777 A 002022 001000 A	639 640	LDAI	05122 02777	EXECUTE A NON 1/0, NON STORE, ONE WORD INS IN LAST LOC. OF UPA.	T01	00639 00640
002022 001000 A 002023 002777 A 002024 000000 A 002025 006010 A	641 642 TX05 643	JMP ENTR LDAI	06010	TEST 5	0 1	00641
002025 006010 A 002026 006010 A 002027 052776 A 002030 001000 A	644 645	STA JMP	02776 02776	EXECUTE A TWO WORD INST. IN LAST UPA	0 1	00643 00644 00645
00-2031 002776 A 0/ 32 000000 A 0. 33 006010 A	646 TX06 647	ENTR LDAI	06010	TEST 6 EXECUTE A TWO WORD INST WITH 1ST WORD IN	0 1	00645 00646 00647
002034 006010 A 002035 052777 A 002036 001000 A	648 649	STA JMP	027 <b>77</b> 027 <b>77</b>	LAST UPA.	0 1	00647 00648 00649
002037 002777 A 002040 000000 A	650 TX07	ENTR	ASTOCIONES DE TE	TEST 7		00650

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002041 006010 A 002042 001010 A	651	LDAI	01010	STORE 2 WORD JUMP TEST (DO NOT MEET CONDI	T 0 1	00651
002043 052776 A 002044 001000 A 002045 002776 A	652 653	STA	02776 02776	IONS>	0 1 0 1	00652 00 <i>6</i>
002046 0000000 A 002047 006010 A 002050 003000 A	654 TX10 655	ENTR LDAI	03000	TEST 10 EXECUTE A XEC WITH 2ND WORD IN LAST LOC		00655
002051 052776 A 002052 006010 A 002053 002057 A	656 657	STA LDAI	027 <b>76</b> *+5	OF UPA AND EXECUTING A HALT COMMAND.	0 1 0 1	00656 00657
002054 052777 A 002055 001000 A 002056 002776 A	658 659	STA	02777 02776		0 1 0 1	00658 00659
002057 0000000 A 002060 0000000 A 002061 006010 A 002062 006047 A	660 661 TX11 662	HLT ENTR LDAI	006047	TEST 11	0 1 0 1 0 1	00660 00661 <b>0</b> 0662
002063 052776 A 002064 012060 A 002065 006110 A 002066 100000 A	663 664 665	STA LDA ORAI	02776 TX11 0100000	PROTECTED TO UNPROTECTED MEMORY WITH 2ND HORD OF INR IN LAST UPA.	0 1	00663 00664 00665
002067 052777 A 002070 001000 A 002071 002776 A	666 667	STA JMP	02777 02776		0 1 0 1	00666 00667
002072 000000 A 002073 006010 A 002074 006017 A	668 TX12	ENTR LDAI	06017	TEST 12 EXECUTE A 2 WORD READ WITH 2ND WORD IN		00668 00669
002075 052776 A	670 671	STA	02776	LAST UPA		00670
002077 052777 A 002100 001000 A 002101 002776 A	672	TZA STA JMP	02777 02776		0 1	00671 00672 00673
002102 000000 A 002103 006010 A 002104 006057 A	674 TX13 675	ENTR LDAI	06057	TEST 13 EXECUTE A EXTENDED WRITE TO UNPROTECTED	0 1 0 1	00674 00675
002105 052776 A 002106 006010 A 002107 002102 A	676 677	STALDAI	02776 TX13	MEMORY WITH 2ND WORD OF INST. IN LAST LOC. OF UPA	0 1 0 1	00676 00677
002110 052777 A 002111 001000 A 002112 002776 A	678 679	AT2 9ML	02777 02776		0 1 0 1	00678 00679
002113 0000000 A 002114 100745 A 002115 002000 A 002116 005035 A	680 TX14 681 682	ENTR EXC CALL	0700+MPM ERRR	TEST 14 Execute 1 WORD I/O INSTRUCTION IN UPA	0 1 0 1 0 1	00680 00681 00682
002117 100745 A 002120 000000 A 002121 003000 A 002122 002125 A	683 TX15 684 TX16 685	EXC ENTR XEC	0700+MPM *+4	EXECUTE COMMAND BY XEC FROM PA TEST 16 EXECUTE A 1 WORD I/O INST. IN UPA BY		00 00 00685
002123 002000 A 002124 005035 A	686	CALL	ERRR	A XEC IN UPA	0 1	00686
002125 100745 A 002126 0000000 A 002127 012126 A 002130 052132 A 002131 003000 A	687 688 TX17 689 690 691	EXC ENTR LDA STA XEC	0700+MPM TX17 *+2	DISABLE MP TEST 17 EXECUTE A 1 HORD I/O INST. IN A PA BY A XEC IN UPA	0 1 0 1 0 1 0 1 0 1	19프로핑프트 공연 1912년 등 <u>191</u> 2일
002132 002131 A 002133 002000 A 002134 005035 A	692	CALL	ERRR		0 1	00692
002135 000000 A 002136 006010 A 002137 101101 A	693 TX20 694	ENTR LDAI	0101101	TEST 20 Execute 2 word I/O INST IN LAST LDC.	0 1 0 1	00693 00694
002140 052776 A 002141 006010 A 002142 002774 A	695 696	STA	02776 02774	OF UNPROTECT MEMORY.		00695 00696
002143 052777 A 002144 006010 A 002145 001000 A	697 698	STA LDAI	02777			00697 00698
002146 052774 A 002147 006010 A 002150 005035 A	699 700	STALDAI	02774 ERRR			00699 00700
002151 052775 A 002152 001000 A 002153 002776 A	701 702	STA	027 <b>75</b> 027 <b>76</b>			00701 00702
002154 0000000 A 002155 005201 A 002156 050010 A 002157 002000 A 002160 005035 A	703 TX21 704 705 706	ENTR COMP STA CALL	1 0 1 0 ERRR	TEST 21  EXECUTE A 1 WORD WRITE INST IN UPA THAT WRITE IN PROTECTED CORE.	0 1 0 1	00703 00704 00705 00706
002161 <b>0</b> 000000 A 002162 006057 A	707 TX22 708	ENTR STAE#	. <b>*</b> − 1	TEST 22 EXECUTE A 2 WORD WRITE INST. IN UPA		00707 00708
002163 102161 A 002164 002000 A 002165 005035 A	709	CALL	ERRR	THAT WRITES IN PROTECTED CORE.	0 1	00709
002166 000000 A 002167 012166 A 002170 005111 A 002171 052175 A 002172 000000 A 002173 002000 A	710 TX23 711 712 713 714 715	ENTR LDA IAR STA HLT CALL	TX23 *+4 ERRR	TEST 23 EXECUTE A 1 WORD WRITE INST. IN UPA. THAT WRITES IN UPA.	0 1 0 1 0 1 0 1	00710 00711 00712 00713 00713
002174 005035 A 002175 000000 A	716	DATA	. 0			00716
002176 000000 A 002177 012166 A	717 TX24 718	ENTR LDA	TX23	TEST 24 EXECUTE A 2 WORD WRITE INST. IN UPA	0 1	00717 00718

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002200 005111 A 002201 006057 A	719 720		IAR STAE	<b>*</b> +5	THAT WRITES IN UPA.		00719 00720
0 202 002206 A 1 203 000000 A 0 204 002000 A	721 722		HLT CALL	ERR <b>R</b>			00721 00722
002205 005035 A 002206 000000 A 002207 000000 A 002210 010012 A 002211 003000 A 002212 002215 A	723 724 725 726	TX25	DATA ENTR LDA XEC	10 *+4	TEST 25 EXECUTE A WRITE INST. IN UPA VIA A XEC COMMAND IN UPA WHICH TRIES TO	0 1 0 1	00723 00724 00725 00726
002213 002000 A 002214 005035 A	727		CALL	ERRR	MODIFY A PA LOC.		00727
002215 050012 A 002216 000000 A 002217 012226 A 002220 003000 A 002221 002225 A	728 729 730 731	TX26	STA ENTR LDA XEC	10 *+7 *+5	TEST 26 EXECUTE A WRITE INST. IN UPA VIA A XEC COMMAND IN UPA WHICH MODIFIES	0 1 0 1	00728 00729 00730 00731
002222 000000 A	732 733		HLT CALL	ERR <b>R</b>	A UPA LOC. (NO INTERRUPT)		00732 00733
002224 005035 A 002225 052226 A 002226 005000 A 002227 000000 A 002230 006010 A	734 735 736 737	TX27	STA NDP ENTR LDAI	*+1 050012	TEST 27 EXECUTE A 1 WORD WRITE INTO PROTECTED	0 1 0 1	00734 00735 00736 00737
002231 050012 A 002232 052777 A	738		STA	02777	MEMORY WHERE THE WRITE INSTRUCTION IS		00738
002233 010012 A 002234 001000 A 002235 002777 A	739 740		LDA JMP	012 02777	IN THE LAST LOC. OF UNPROTECT MEMORY.		00739 00740
002236 000000 A 002237 001000 A 002240 102236 A	741 742	TX30	ENTR JMP*	TX30	TEST 30 EXECUTE A JUMP TO PROTECTED MEMORY.		00741 00742
002241 000000 A 002242 002000 A 002243 102241 A	743 744	TX31	ENTR JMPM#	TX31	TEST 31 EXECUTE A JUMP AND MARK TO PROTECTED MEM.		00743 00744
002244 000000 A 002245 006010 A	745 746	TX32	ENTR LDAI	01000	TEST 32 EXECUTE A JUMP INST. IN UPA WITH 1ST		00745 00746
002246 001000 A 002247 052777 A 002250 001000 A	747 748		STA	027 <b>77</b> 02777	HORD IN LAST LOC. OF UPA.		00747 00748
002251 002777 A 002252 000000 A 002253 022252 A 002254 006706 A 0 '55 000000 A	749 750 751	LX33	ENTR LDB DATA	TX33 006706,0	TEST 33  EXECUTE A INDEXED JUMP INST. IN UPA  MEMORY CAUSING A JUMP TO PROTECTED MEM	0 1	00749 00750 00751
002256 000000 A 002257 006506 A 002260 102256 A	752 753 754 755		ENTR DATA	06506,(TX34)*	TEST 34 EXECUTE A JUMP UNCONDITIONAL AND SET	0 1 0 1	00752 00753 00754 00755
002261 005000 A	756 757		NOP		RETURN IN B. FROM UPA TO PA.		00756 00757
	758 759 760	*		TV24.2		0 1	00758 00759
002262 000000 A 002263 005001 A 002264 006441 A 002265 102262 A	761		JSR* ENTR TZA DATA	006441.(TX35)	TEST 35 EXECUTE THE BIT TEST INST. IN UPA CAUSING UNDER TO PROTECTED MEMORY (OPTIONAL)	0 1 0 1 0 1	00760 00761 00762 00763 00764
002266 000000 A 002267 012266 A 002270 052776 A 002271 006010 A 002272 001000 A	765 766 767 768 770		ENTR LDA STA LDAI	TX36 02776 01000	JUMP TO CALLING PROGRAM TO CHECK P ADDRESS	0 1 0 1 0 1 0 1	00765 00766 00767 00768 00769
002273 052775 A 002274 001000 A 002275 002775 A	771 772		ATZ	027 <b>75</b> 027 <b>75</b>			00771 00772
	773 774 775 776	* TES	SRE	TIONAL <b>ON</b> 620/F 010, TX37		0 1 0 1 0 1	00774 00775 00776
002276 000000 A 002277 006010 A 002300 006617 A	778	TX37	LBAI	006617	TEST 36 TEST SKIP IF REG. EQUAL.		00777 00 <b>7</b> 78
002301 052774 A 002302 006010 A 002303 002276 A	779 780		STALDAI	02774 TX37	EXECUTE A SKIP IF REG. EQUAL WHERE THE A REG. IS EQUAL TO THE VALUE IN		00779 00780
002304 052775 A 002305 005001 A 002306 052776 A 002307 052777 A 002310 012276 A 002311 001000 A 002311 002774 A	781 782 783 784 785 786		STA TZA STA STA LDA JMP	02775 02776 02777 TX37 02774	THE OPERAND ADDRESS, AND THE INSTR. SKIP IS EXECUTED. THE LOCATION SKIPPED TO IS PROTECTED.	0 1 0 1 0 1 0 1	00781 00782 00783 00784 00785 00786
[10.부분 47 - 12.10] 가게 하게 보고 있다 1.10 [10.10] 가게 되었다 1.10 [10.1	787	MSG5	DATA	'INSTR. INT.	ADDR. TEST',0106612,0	0 1	00787

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002322 142322 A 002323 127240 A 002324 152305 A 002325 151724 A 002326 106612 A 002327 000000 A 002330 146720 A 002331 120324 A 002332 142723 A 002333 152240 A 002334 141717 A	788	MSG7	DATA	•MP TEST COMP	LETE',010661	2.0		. 81	00788
002335 146720 A 002336 146305 A 002337 152305 A 002340 106612 A 002341 0000000 A 002342 147720 A 002343 152311 A 002344 147716 A 002345 140714 A 002345 140714 A 002346 120311 A 002351 120320 A 002351 120320 A 002352 151305 A 002353 151705 A 002354 147324 A 002355 120240 A 002355 130256 A	789	MSG8	DATA	OPTIONAL INS	T. PRESENT	0.=YES, 1.	=NO',0106612,0	0 1	00789
002357 136731 A 002360 142723 A 002361 126240 A 002362 130656 A 002363 136716 A 002364 147640 A 002365 106612 A 002366 000000 A 002370 142716 A 002371 152305 A 002372 151240 A 002373 141720 A 002374 152640 A 002375 152331 A 002376 150305 A 002376 150305 A 002377 120240 A 002376 150305 A 002376 150305 A 002377 120240 A 002377 120240 A 002377 120240 A 002377 120240 A	790	MSG9	DATA	0106612, ENTE	R CPU TYPE	0=620/F	1=V73',0106612	. 0 1	00790
002411 000000 A 003000 002000 A	791 792 793		EJEC ORG CALL	03000 ERRR				0 1 0 1 0 1	00791 00792 00793
003001 005035 A 003002 002000 A 003003 005035 A	794		CALL	ERRR				0 1	00794
003003 003033 A	795 796 797 798 799 801 802	¥ * * * *	ZIHT	IS THE 620/F P LOCATIONS 01 AS THE UNPROT ADDRESSES	MEMORY PROTEC 1777 TO 02777 FECTED MEMORY	TEST PR	BY THE PROGRAM	*01 *01 *01 *01 *01	00795 00796 00797 00798 00799 00800 00801
003010 003010 010000 A 003011 051224 A 003012 010001 A 003013 051225 A 003014 005001 A 003015 051233 A	803 804 805 806 807 808 809	MPTS	DRG LDA STA LDA STA TZA STA LDBI	03010 LDCZ 1 LDCZ+1 HLTF 2	SAVE LOCATI	ONS ZERO A		0 1 0 1 0 1 0 1 0 1 0 1	00803 00804 00805 00806 00807 00808 00809
003017 000002 A 003020 005021 A 003021 006110 A	811	MPTO	TBA ORAI	0400	TO 0		T LOCATIONS 040 L RESTART INTER		00811
003022 000400 A 003023 056000 A	813		STA	0,2	1	ESSES).	E KESIMKI TIJIEK	0 1	00813
003024 005122 A 003025 005021 A 003026 006140 A	814 815 816		IBR TBA SUBI	0 4 0	CHECK IF 04	n		0 1 0 1 0 1	00814 00815 00816
003027 000040 A 003030 001010 A	817		JAZ	*+8	v -				00817
003031 003040 A 003032 006140 A 003033 000340 A	818		SUBI	0340	CHECK IF 04	00 (ALL A	DDRESSES MODIFI	ED) 01	06 3
003033 000340 A 003034 001010 A 003035 003044 A	819		JAZ	*+8					00819
003036 001 <b>000</b> A 003037 003020 A	820		JMP	MPTO				0 1	00850

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003040 006020 A 821	LDBI	044	JUMP OVER PF/R INTERRUPT ADDRESSES	0 1	00821
003041 000044 A 002442 001000 A 822 00 \3 003020 A	JMP	MPTO		0 1	00855
00 44 010442 A 823 003045 001010 A 824	LDA JAZ	\$CON MPCM	TEST IF CONSOLE MODE.		00823
003046 003175 A 003047 006030 A 825	LDXI	MSG1	TEST IT CONSULE NUMBER		00825
003050 001456 A 003051 002000 A 826	CALL	DUTD	WRITE (MEMORY PROTECT TEST)		
003052 100403 A 003053 006030 A 827	LDXI	MSG9	*ENTER CPU TYPE*		00827
003054 002367 A 003055 002000 A 828	CALL*	DUTD	PRINT		00828
003056 100403 A 003057 002000 A / 829	CALL#	INPG	INPUT DCTAL NUMBER		00829
003060 100416 A 003061 001000 A 830	JMP	MPTS	TERMINATION EXIT		00830
003062 003016 A 003063 001000 A 831	JMP	*-8	ABORT EXIT		00831
003064 003053 A 003065 001000 A 832	JMP	<b>*</b> -2	COMMA EXIT		00832
003066 003063 A 003067 006057 A <b>833</b>	STAE	DPTX	SAVE CPU/M.P. TYPE	0 1	00833
003070 005123 A 003071 002000 A 834	CALL*	DUTC	CR/LF	0 1	00834
003072 100402 A 003073 001016 A <b>835</b>	JANZ	MPC1	SKIP OPTIONAL INST QUERY IF V73	0 1	00835
003074 003115 A 003075 006030 A 836	LDXI	MSG8		0 1	00836
003076 002342 A 003077 002000 A 837	CALL*	атиа	WRITE -OPTIONAL INST. PRESENT 0=YES: 1=NO	0 1	00837
003100 100403 A 003101 002000 A 838	CALL*	INPG	INPUT OCTAL NUMBER.	0 1	00838
003102 100416 A 003103 001000 A 839	JMP	MPTS	TERMINATION EXIT	0 1	00839
003104 003016 A 003105 001000 A 840	JMP	<b>*</b> - 8	ABORT EXIT	0 1	00840
003106 003075 A 003107 001000 A 841 003110 003105 A	JMP	<b>*</b> -2	COMMA EXIT	0 1	00841
003111 006057 A 842 003112 005122 A	STAE	OPTT		0 1	00842
003112 002000 A 843 003114 100402 A	CALL*	DUTC	CR/LF	0 1	00843
0r 15 006030 A 844 P	IPC1 LDXI	MSG4	START TEST 0=MASK REG OR 1=INST TEST	0 1	00844
000117 002000 A 845	CALL*	DUTD		0 1	00845
003121 002000 A 846 003122 100416 A	CALL*	INPG	INPUT OCTAL NUMBER.	0 1	00845
003123 001000 A 847 003124 003016 A	JMP	MPTS	TERMINATION EXIT	0 1	00847
003125 001000 A 8 <b>48</b> 003126 003115 A	JMP	*-8	ABORT INPUT		00848
003127 001000 A 849 003130 003125 A	JMP	<b>*</b> -2	COMMA EXIT		00849
003131 006057 A 850 003132 005121 A	STAE	TEST			00850
003133 051104 A 851 003134 002000 A 852 003135 100402 A	STA CALL*	MODE	BUTPUT CR/LF	0 1	
003136 010442 A 8 <b>53  </b> 003137 001010 A 8 <b>54</b> 003140 003175 A	1PT1 LDA Jaz	\$CON MPCM	TEST IF CONSOLE MODE.		00853 00854
003141 006030 A 855 003142 001472 A	LDXI	W205		0 1	00855
003143 002000 A 856 003144 100403 A	CALL	DUTD	WRITE (CYCLES =)	0 1	00856
003145 005001 A <b>857</b> 003146 051633 A <b>858</b> 003147 002000 A <b>859</b>	TZA STA CALL*	WTMS INPG	INPUT OCTAL NUMBER.	0 1	00857 00858 00859
003150 100416 A 003151 001000 A 860	JMF	MPZZ	TERMINATION EXIT	0 1	00860
003152 003171 A 003153 001000 A 861 003154 003136 A	JMP	MPT1	ABORT INPUT	0 1	00861
003155 001000 A 862 003156 003162 A	JMP	MPT2	COMMA EXI	0 1	00862
003157 051634 A 863 003160 001000 A 864 003161 003165 A	ATZ	CYCL *+5	PERIOD EXIT WIMS = 0.(A) = CYCL		00863 00864
003162 041633 A 865 M	1PT2 INR JMP	WTMS *-4	COMMA WIMS = 1, (A) = CYCL		00865 00866
003164 003157 A 003165 002000 A <b>867</b> 003166 100402 A	CALL	DUTE	OUTPUT CR/LF	0 1	00867
003166 100402 H 003167 001000 A 868 0 70 003207 A	JMP	MPT3		0 1	00868
0 71 002000 A 869 1 003172 100402 A	1PZZ CALL*	DUTC		0 1	00869
003173 001000 A 870 003174 003136 A	JMP	MPT1		0 1	00870
003175 005001 A 871 N 003176 005002 A 872	1FCM TZA TZB		CONSOLE MODE CLEAR VOLATILE REGISTERS.		0:271

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003200	005004 A 051633 A 000077 A	87 87	4 5	TZX STA HLT	WTMS 077		0 1 0 1 0 1	00873 00874 00875
003503	051634 A 006077 A 005121 A	87 87		STA STX <b>e</b>	TEST	NO OF CYCLES TO RUN TEST 0 = MASK, 1 = INST. TEST	01	00%
003205	006067 A	87	В	STBE	OPTT	0 = HARDWARE PRESENT, 1 = NO HARDWARE	0 1	00878
003207	006010 A 001230 A	90°20	9 MPT3	LDAI	HLTR	SET UP OPERATOR CONSOLE INTERRUPT TO HLTR	0 1	00879
003212	050001 A 006010 A 002000 A	88	1	STA LDAI	0200 <b>0</b>	ROUTINE, NOT BACK TO EXECUTIVE.	01	00880
003215	050000 A 006010 A 001000 A	88		LDAI	01000	SET UP INITIAL CONDITIONS	01	00883
003217	051641 A 006010 A 001777 A	88	(37)	STA LDAI	01777		0 1 0 1	00884 00885
003223		88		STA LDAI	HLDC 01		01	00886 00887
003225	000001 A 051643 A 006010 A 002000 A	88		STA LDAI	MASK 02000	STORE JUMP AND MARK IN INTERRUPT ADDRESSES	0 1 0 1	00888 00889
003230 003231 003232 003233 003234 003235 003236	050020 A 050022 A 050024 A 050026 A 050030 A 050032 A 050034 A		1 2 3 4 5 6	STA STA STA STA STA STA	0020 0022 0024 0026 0030 0032 0034	FOR MEMORY PROTECT  SET SUBROUTINE ERRS ARE MP INTERRUPT	0 1 0 1 0 1 0 1 0 1 0 1	00890 00891 00892 00893 00894 00895 00895
003241	002000 A 001133 A	89	9	CALL	ILAS, 0377, ERR	- CONTROL	0 1	00899
003243	000377 A 000746 A 006017 A	90	n	LDAE	TEST	CHECK WHICH TEST TO START EXECUTION.	0 1	00900
	005121 A	90	ñ	JAZ	*+4	CHECK MITCH FEST TO STAKE ENECOTEDIA	0 1	00901
003250	003252 A	90	2	JMP	MPT6		0 1	00902
003253	003323 A 010442 A 001010 A 003264 A	90 90		LDA Jaz	\$CON MPT4	CHECK IF CONSOLE MODE		00903
	011633 A			L DA JAZ	MTMS MPT4	CHECK IF NON-ERROR MESSAGE SUPPRESSION	0 1 0 1	00905 00906
003260	003264 A	90	7	LBXI	M2G3		0 1	00907
하나면 하다 바다에 되어야기 가입하다 남아를 하다고 있다.	001477 A 002000 A 100403 A	90	8	CALL*	DUTD	WRITE (MASK REGISTER TEST)	0 1	00908
00000	100400 11		1 * MA	SK REGIS		*	0 1 0 1 0 1	00909 00910 00911
003264	002000 A	91 91	T	CALL	**************************************	*** MASK REGISTER TEST	0 1 0 1 0 1	00912 00913 00914
003265	000772 A			LDA	SMEM	MASK REGISTER TEST	554000 554000	00915
003267 003270 003271	141642 A 001010 A 003305 A	91 91	6 7	SUB JAZ	MPT8	D	0 1 0 1	00916 00917
003272 003273 003274	005111 A	91 91 92	9	LDA IAR STA	HLOC			00918 00919 00920
003275	006120 A 000777 A	92	1	ADDI Sta	027 <b>7</b> HLOC		0 1	00921
003301	041643 A 002000 A 001204 A	92 92		INR CALL	CKSE	CHECK SS3 AND HLTF FLAG.	0 1 0 1	00923 00924
003303	001000 A 003264 A	92	5	JMF	MPT4	PROCESS NEXT BLOCK	0 1	00925
003305 003306	011634 A 005311 A	95 95		L D A D A R	CYCL	CHECK IF CYCLE MODE D		00927
003310	001004 A		207	JAN	MPT5	CONTINUAL MOBE		00928
003312	003321 A	92	0	JXZ	MODE MPT5	D D D	0 1	
003315	001010 A 005024 A 051634 A			JAZ Sta	CYCL	CYCLES NOT COMPLETED D	01	
003317	001000 A 003321 A	93	3	JMP	MPT5	D D	0 1	00933
		93 93 93 93	7 * IN	STRUCTIO	**************************************		0 1 0 1 0 1 0 1 0 1	00 00 00936 00937 00938 00940

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003321 002000 A 003322 001204 A	941	MPT5	CALL	CKSE		0 1	00941
043323 006010 A ' 324 000001 A	942	MPT6	LDAI	1	SET UP ERROR CODE OF ONE ** 1 **	0 1	00942
003326 005001 A	943 944		STA TZA	ERRC		0 1	00943 00944
003327 006057 A 003330 005121 A 003331 002000 A	945 946		STAE	CLMP	PROTECT ALL CORE		00945
003332 001234 A 003333 010442 A	947		LDA	\$CON	CHECK IF CONSOLE MODE		00946
003334 001010 A 003335 003345 A	948		JAZ	MPT7		0 1	00948
003336 011633 A 003337 001010 A 003340 003345 A	949 9 <b>5</b> 0		JAZ	MPT7	CHECK IF TELETYPE HON-ERROR MESS. SUPPRESS		00949 00950
003341 006030 A 003342 002313 A	951		LDXI	MSG5	WRITE (INST. INTERRUPT ADDRESS TEST.)	0 1	00951
003343 002000 A 003344 100403 A	952		CALL	DUTD		0 1	00952
003345 006010 A 003346 177773 A	Post Contraction	MPT7	LDAI	0177773	UNPROTECTED 3RD BLOCK (02000 TO 02777)		00953
003347 100045 A 003350 103145 A 003351 100745 A	954 955 956		EXC DAR EXC	MPM MPM 0700+MPM	SELECT MASK REGISTER O DUTPUT TO MASK REGISTER O DISABLE MP	0 1 0 1	00954 00955 00956
003352 006010 A 003353 003370 A 003354 051645 A	957 958		LDA <b>I</b> Sta	ML01 LDDP	SET UP LOOP ADDRESS		00957 00958
003355 006010 A 003356 003423 A	959		LDAI .	MG02-1	SET UP CONTINUE ADDRESS		
003357 051646 A 003360 002000 A 003361 001133 A 003362 000377 A	960 961		STA	ILAS, 0377, ERR	R SET ALL INTERRUPT ABBRESS TO ERROR SUB.	01	00960 00961
003363 005035 A 003364 002000 A 003365 001133 A 003366 000001 A	962		CALL	ILAS, HALT, MCO		0 1	00962
003367 003375 A 003370 100645 A 003371 002000 A	963 964	ML01	EXC CALL	0600+MPM TX01	ENABLE MP EXECUTE A HALT INST. IN UNPROTECTED CORE		00963 00964
003372 002000 A 003373 002000 A 003374 005035 A	965		CALL	ERRR		0 1	00965
003375 000000 A 0 100745 A 377 006017 A	966 967 968	MC02	ENTR EXC LDAE	0700+MPM DPTX	DISABLE MP GET CPU TYPE	0 1	00966 00967 00963
0.1400 005123 A 003401 001016 A	969			MPC2	JUMP IF V73		00963
003402 003412 A	970	* 620	52 Alberton	MEMORY PROTECT	*** 이 없는 100 전에 가는 사람이 있다는 보고 있다면 하다면 하다면 하다면 하다면 하다면 하다면 하다면 하다면 하다면 하		00970
003403 013375 A 003404 006140 A 003405 002002 A 003406 001010 A	971		LDA SUBI	MC02 TX01+2	CHECK IF INTERRUPTED FROM CORRECT ERROR LOCATION.	0 1	00971
003407 003424 A 003410 001000 A	973 974		JAZ JMP	MG02 MPC3	ERROR		00973 00974
003411 003417 A	975		TYPE ME	MORY PROTECT		0 1	00975
003412 013375 A 003413 006140 A 003414 002001 A	977	MPC2	SUB I	MC82 TX01+1	CHECK IF INTERRUPTED FROM CORRECT ERROR LOCATION	0 1	00976 00977
003415 001010 A 003416 003424 A 003417 011644 A	978	MPC3	JAZ LDA	MG02 ERRC			00978 00979
003420 121102 A 003421 051644 A 003422 002000 A	980 981 982	03	ADD STA CALL	N100 ERRC ERRR		0 1 0 1	00980 00981 00982
003423 005035 A 003424 002000 A		MG02	CALL	ERLP			00983
003425 005104 A 003426 011644 A 003427 151103 A	984 985		L D A A N A	ERRC C100			00984 00985
003430 051644 A 003431 041644 A 003432 006010 A	986 987 988		STA INR LDAI	ERRC ERRC ML02	ERROR COUNT 2 TEST NO 2 ** 2 **	0 1 0 1 0 1	00986 00987 00988
003433 003444 A 003434 051645 A 003435 006010 A	989 990		STA	LDOP MC03	SET UP LOOP ADDRESS	0 1 0 1	00989
003436 003447 A 003437 051646 A 003440 002000 A 003441 001133 A 003442 000001 A	991 992		STA CALL	CONT ILAS, HALT, MCO	SET UP CONTINUE ADDRESS 3 SET UP HALT INTERRUPT TO CONTINUE	0 1 0 1	00991 00992
003443 003447 A 003444 100645 A 003445 002000 A	993 994	WF05	EXC CAL <b>L</b>	0600+MPM TX02	ENABLE MP EXECUTE HALT INST. IN LAST UPA		00993 00994
003446 002004 A f 447 000000 A , <b>4</b> 50 100745 A 003451 002000 A	995 996 997	MC03	ENTR EXC CALL	0700+MPM ERLP	DISABLE MP	0 1	00995 00996
003452 005104 A 003453 041644 A	998		INR	ERRC	ERROR COUNT 3 TEST NO. 3 ** 3 **		00997 00998
003454 006010 A 003455 003466 A	999		LDAI	ML03			00999

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003456 051645 A	1001	STA	MC04	SET UP LOOP ADDRESS	10000	$01000\\01001$
003463 001133 A	1002	STA	CONT ILAS, HALT, MCO	SET UP CONTINUE ADDRESS AFTER INTERRUPT 4 SET UP HALT INTERRUPT TO CONTINUE		010
003465 003473 A 003466 100645 A 003467 002000 A	1005	EXC CALL	0600+MPM TX03	ENABLE MP EXECUTE A XEC OF HALT INST.	- SQ11 STEEL	01004 01005
003470 002011 A 003471 002000 A 003472 005035 A	1006	CALL	ERRR		0 1	01006
003473 0000000 A 003474 100745 A 003475 002000 A 003476 005104 A	1008	ENTR EXC CALL	0700+MPM ERLP	BISABLE MP	0 1	01007 01008 01009
003477 041644 A	1010	INR LDAI	ERRC ML04	ERROR COUNT 4 TEST NO. 4 ** 4 **	0 1 0 1	$01010 \\ 01011$
003501 003516 A 003502 051645 A 003503 006010 A	1012	STALDAI	L00P MG05-1	SET UP LOOP ADDRESS		01012
003504 003534 A 003505 051646 A 003506 002000 A 003507 001133 A	1014	STA	CONT ILAS, HALT, ERR	SET UP CONTINUE ADDRESS		01014 01015
003513 001133 A	1016	CALL	ILAS, DVER, MCO	5 SET UP CONTINUE ADDRESS AFTER INTERRUPT	0 1	01016
003515 003521 A 003516 100645 A 003517 002000 A 003520 002016 A	1017 ML04 1018	CALL	0600+MPM TX04	ENABLE MP EXECUTE 1 WORD INST. IN LAST UPA		01017 01018
003521 000000 A 003522 100745 A 003523 013521 A	1019 MC05 1020	ENTR EXC LDA SUBI	0700+MPM MC05 03000	DISABLE MP CHECK INTERRUPT ADDRESS	0 1 0 1	01019 01020 01021 01022
003525 003000 A 003526 001010 A 003527 003535 A	1023	JAZ	MG05		0 1	01023
003530 011644 A 003531 121102 A 003532 051644 A 003533 002000 A	1024 1025 1026 1027	LDA ADD STA CALL	ERRC N100 ERRC ERRR		0 1 0 1 0 1	01024 01025 01026 01027
003534 005035 A 003535 002000 A 003536 005104 A	1028 MG05	CALL	ERLP		0 1	010
003537 011644 A 003540 151103 A 003541 051644 A 003542 041644 A	1029 1030 1031 1032 1033	LDA ANA STA INR LDAI	ERRC C100 ERRC ERRC ML05	ERROR COUNT 5 TEST 5 ** 5 **	0 1 0 1 0 1	01029 01030 01031 01032 01033
003545 051645 A	1034 1035	STA LDAI	MC06	SET UP LOOP ADDRESS		01034 01035
003547 003560 A 003550 051646 A 003551 002000 A 003552 001133 A 003553 000020 A	1036	STA	CONT ILAS, DVER, MCO	SET UP CONTINUE ADDRESS AFTER INTERRUPT		01036 01037
003554 003560 A 003555 100645 A 003556 002000 A 003557 002024 A	1039	EXC	0600+MPM TX05	ENABLE MP EXECUTE 2 WORD INST. IN LAST UPA		01038 01039
003560 000000 A 003561 100745 A 003562 002000 A	1048 MC06 1041 1042	ENTR EXC CALL	0700+MPM ERLP	DISABLE MP	0 1	01040 01041 01042
003563 005104 A 003564 041644 A 003565 006010 A	1043 1044	INR LDAI	ERRC ML06	ERROR COUNT 6 TEST 6 ** 6 **		01043 01044
003566 003577 A 003567 051645 A 003570 006010 A	1045 1046	STA	LOOP MC07	SET UP LOOP ADDRESS		01045 01046
003571 003602 A 003572 051646 A 003573 002000 A 003574 001133 A 003575 000020 A	1047 1048	STA	CONT ILAS, OVER, MCO	SET UP CONTINUE ADDRESS 7 SET UP CONTINUE ADDR. AFTER INTERRUPT	F5/52/5 A33	01047 01048
003576 003602 A 003577 100645 A 003600 002000 A 003601 002032 A	1049 ML06 1050	EXC	0600+MPM TX06	ENABLE MP EXEC 2 WORD INST. 1ST WORD IN UPA		01049 01050
003602 000000 A 003603 100745 A 003604 002000 A	1051 MC07 1052 1053	ENTR EXC CALL	0700+MPM ERLP	DISABLE MP		01051 01052 01053
003605 005104 A 003606 041644 A 003607 006010 A	1054 1055	INR LDAI	ERRC ML07	TEST 7 ***		01 4
	1056 1057	STALDAI	MC10	SET UP LOOP ADDR.		01056 01057
003613 003624 A 003614 051646 A	2	STA	CONT	SET UP CONTINUE ADDR.	0 1	01058

	m	AINTAIN	III	•	MEPROT	PAC	5E 17
003615 002000 0	a 1059		CALL	ILAS, DVER, MC1	O SET UP CONTINUE ADDRESS AFTER INTERRUPT	ΰi	01059
	4 4 1060 4 1061	ML07	EXC	0600+MPM TX07	ENABLE MP EXECUTE A JUMP INST (NOT MET) IN LAST UPA	0 1 0 1	01060 01061
003624 000000 0 003625 100745 0 003626 002000 0	4 1063 4 1064	MC10	ENT <b>r</b> Exc Call	0700+MPM ERLP	DISABLE MP	0 1	01062 01063 01064
어디어 집에서를 사이라 하고 있습니다. 그 아니라 아이에 가게 되었다. 그리다고 나를 다 먹었다.			INR LDAI	ERRC ML10	TEST 10 ** 10 **	0.070/2020/2019	01065 01066
(20) 사용하게 프라이트 왕조(1) 프랑스프라스프라스크라스크라	A A 1067 A 1068		STA	LOOP MC11	LOOP ADDR.		01067 01068
003635 003664 0 003636 051646 0 003637 006017	A 1069		STA LDA <b>e</b>	CONT	CONTI. ADDR. Get cpu type		01069 01070
003640 005123 0 003641 001016 0 003642 003651	A A <b>1071</b>			*+010	JUMP IF V73		01071
003643 002000 003644 001133 003645 000020	1072 A 1073 A	[2] STOR	R 620/F: CALL	SETUP FOR OVE ILAS, OVER, MC1	RFLOW INTERRUPT.  1 CONTI ADDR. AFTER INTERRUPT.		01072 01073
003646 003664 0 003647 001000 0 003650 003661	A 1074		JMP	ML 1 0	CONTINUE	0 1	01074
003651 002000 003652 001133	1075 A 1076	N35-0 3- 753.00	R V73: SI	ETUP FOR HALT ILAS, OVER, ERR		-0.470 -0.000	01075 01076
003653 000020 003654 005035 003655 002000 003656 001133 003657 000001	A A A 1077 A		CALL	ILAS, HALT, MC1	1 CONTI ADDR AFTER INTERRUPT	0 1	01077
003662 002000	A 1079	ML 10	EXC CALL	0600+MPM TX10	ENABLE MP Execute a xec indir to a halt command		01078 01079
003665 100745	A 1080 A 1081 A 1082	MC11	ENTR EXC CALL	0700+MPM ERLP	DISABLE MP	0 1	01080 01081 01082
0 71 006010	A 1083 A 1084		INR LDAI	ERRC ML11	TEST 11 **		01083 01084
00.372 003703 003673 051645 003674 006010	A 1085 A 1086		STA LDAI	L00P MC12	LOOP ADDR.		01085 01086
003677 002000	A 1087 A 1088 A		STA	CONT ILAS, OVER, MC1	CONTINUE ADDRESS AFTER INTERRUPT		01087 01088
003702 003707 003703 100645 003704 002000	A	ML11	EXC CALL	0600+MPM TX11	ENABLE MP EXECUTE A INR EXTENDEND THRO PA TO UPA	15.2.10.000.0	01089 01090
003706 002060 003707 000000 003710 100745 003711 002000	A 1094	MC12	DATA ENT <b>r</b> EXC CALL	TX11 0700+MPM ERLP	DISABLE MP	0 1 0 1	01091 01092 01093 01094
003712 005104 003713 041644 003714 006010			INR LDAI	ERRC ML12	TEST 12 **		01095 01096
003715 003726 003716 051645 003717 006010	A A 1097 A 1098		STA LDAI	L D D P M C 1 3	LOOP ADDR.	0 1	01097 01098
003720 003732 003721 051646 003722 002000 003723 001133 003724 000020	A 1099 A 1100 A		STA	CONT ILAS, OVER, MC1	CONTINUE ADDRESS AFTER INTERRUPT		01099
003730 002072	A 1101 A 1102		EXC CALL	0600+MPM TX12	ENABLE MP Execute a 2 word read with 2nd word in las		01101 01102
003732 000000 003733 100745 003734 <b>00</b> 2000	A 1105 A 1106	MC13	NOP ENTR EXC CALL	0700+MPM ERLP	DISABLE MP	0 1 0 1	01103 01104 01105 01106
그리는 그렇게 가게하다면 하면 하면 하는 그리지 않아 하면 하다면 하다면 하다면 하다면 하다면 하다 하다 하다.	A 1107 A 1108		INR LDAI	ERRC ML13	TEST 13 **		01107 01108
003740 003751 003741 051645 003742 006010	A 110 <b>9</b> A 1110		STA LDAI	L D O P M C 1 4	LOOP ADDR.		01109
00 43 003755 0 00 44 051646 003745 002000 003746 001133	A 1111 A 1112 A		STA CALL	CONT ILAS, OVER, MC1	CONTINUE ADDR. 4 CONTINUE ADDR. AFTER INTERRUPT		01111
003747 000020 1 003750 003755 1 003751 100645	À	ML13	EXC	0600+MPM	ENABLE MP	0 1	01113

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003752 002000 A	1114	CALL	TX13	EXECUTE EXTENDED WRITE TO UNPROTECTED MEM.	0 1	01114
003754 005000 A 003755 000000 A		NOP ENTR		2ND WORD IN LAST UPA.	0 1 0 1	01 5 5
003756 100745 A	1117 1118	CALL	0700+MPM ERLP	DISABLE MP	0 1	01118
003760 005104 A 003761 041644 A 003762 006010 A	1119	INR LDAI	ERRC ML14	TEST 14 **		01119 01120
003763 004000 A 003764 051645 A 003765 006010 A	1121	STA	LDDP MG15-1	LDOP ADDR		01121
003766 004017 A 003767 051646 A 003770 002000 A 003771 001133 A	1123	STA	CONT ILAS, OVER, ERRE	CONTINUE ADDR.	10.254 (4) -2.41	01123 01124
003772 000020 A 003773 005035 A 003774 002000 A 003775 001133 A 003776 000002 A	1125	CALL	ILAS, IDE, MC15	CONTINUE ADDR. AFTER INTERRUPT	0 1	01125
003777 004003 A 004000 100645 A 004001 002000 A	1126 ML14 1127	EXC	0600+MPM TX14	ENABLE MP	and the second second	01126 01127
004002 002113 A 004003 0000000 A 004004 100745 A 004005 006017 A	1129	ENTR EXC LDAE	0700+MPM MC15	DISABLE MP	0 1	01128 01129 01130
004006 004003 A 004007 006140 A		SUBI	TX14+2	CHECK IF INTERRUPT LOC. CORRECT	AT ELECTRICAL	01131
004010 002115 A 004011 001010 A 004012 004020 A	1132	JAZ	MG15		0 1	01132
004013 011644 A 004014 121102 A 004015 051644 A 004016 002000 A	1133	LDA ADD STA CALL	ERRC N100 ERRC ERRR		0 1 0 1 0 1 0 1	01133 01134 01135 01136
004017 005035 A 004020 002000 A		CALL	ERLP		0 1	01137
004021 005104 A 004022 011644 A 004023 151103 A	1138	LDA	ERRC C100		0 1	01138 01139
004024 051644 A 004025 041644 A 004026 006010 A 004027 004040 A	1141	STA INR LDAI	ERRC ERRC ML15	TEST 15 ** 15 **	0 1 0 1 0 1	01140 01141 01142
004030 051645 A 004031 006010 A	1143 1144	STA LDAI	L00P MC16-1	LODP ADDR.		01144
004032 004042 A 004033 051646 A 004034 002000 A 004035 001133 A 004036 000002 A	1145	STA CALL	CONT ILAS, IDE, ERRR	CONTI ADDR. SHOULD NOT GET A INTERRUPT		01145 01146
004037 005035 A 004040 100645 A 004041 003000 A	1147 ML15	EXC	0600+MPM TX15	ENABLE MP Disable inst. in upa		01147 01148
004042 002117 A		CALL	ERLP		0 1	01149
004044 005104 A 004045 041644 A 004046 006010 A	1150	INR LDAI	ERRC ML16	TEST 16 ** 16 **		01150 01151
004047 004060 A 004050 051645 A 004051 006010 A 004052 004063 A		STA	LDDP MC17	LOOP ADDR.		01152 01153
004053 051646 A 004054 002000 A 004055 001133 A 004056 000002 A	1154 1155	STA		CONTINUE ADDR. AFTER INTERRUPT		01154 01155
004057 004063 A 004060 100645 A 004061 002000 A 004062 002120 A	그 사람들은 이렇게 하면서 하면 하면 하는 것 같아. 그 나는 사람들이 그 사람은 살아 먹었다.	EXC CALL	0600+MPM TX16	ENABLE MP EXECUTE I/O VIA XEC IN UPA		01156 01157
004063 000000 A 004064 100745 A 004065 002000 A	1159 1160	ENTR EXC CALL	0700+MPM ERLP	DISABLE MP	0 1	01158 01159 01160
004066 005104 A 004067 041644 A 004070 006010 A 004071 004102 A	1161 1162	INR LDAI	ERRC ML17	TEST 17 **		01161
004072 051645 A 004073 006010 A	1163 1164	STA	MCS0	LOOP ADDR.		01163
004074 004106 A 004075 051646 A 004076 002000 A 004077 001133 A	1165 1166	STA	CONT ILAS, IDE, MC20	CONTINUE ADDR. AFTER INTERRUPT	13202355	01165 01166
004100 000002 A 004101 004106 A 004102 100645 A 004103 002000 A 004104 002126 A	1167 ML17 1168	EXC	0600+MPM TX17	ENABLE MP EXECUTE A I/O INST IN PA BY A XEC IN UPA		0 1 1 6 L
004105 100745 A 004106 000000 A	1169 1170 MC20	EXC ENTR	0700+MPM	DISABLE MP	(Car. 1879)	01169
004107 100745 A		CALL	0700+MPM ERLP	DISABLE MP	0 1	

7.65

	01173 01174 01175	3
	0117	1
1 115 051645 A 1175 STA LOOP LOOP ADDR. 01	0117	5
004117 004150 A 004120 051646 A 1177 STA CONT CONTINUE ADDR. 01	0117	7
004123 000002 A 004124 005035 A	0117	9
004130 004134 A 004131 100645 A 1180 ML <b>20</b> EXC	0118	
004135 100745 A 1183 EXC 0700+MPM DISABLE MP	0118 0118 0118	3
004137 004134 A	0118	
	0118	6
004145 121102 A 1188 ADD N100 004146 051644 A 1189 STA ERRC	0118 0118 0118 0119	9
004150 005035 A 004151 002000 A 1191 MG21 CALL ERLP	0119	
004154 151103 A 1193 ANA C100 01 004155 051644 A 1194 STA ERRC 01 004156 041644 A 1195 INR ERRC TEST 21 ** 01	0119 0119 0119 0119 0119	3 4 5
004161 051645 A 1197 STA LOOP LOOP ADDR. 004162 006010 A 1198 LDAI MG22-1	0119 0119	
	0119 0120	
0 .70 005035 A 004171 002000 A 1201 CALL ILAS, WRT, MC22 CONTINUE ABBR. AFTER INTERRUPT 01 004172 001133 A 004173 000004 A	0120	1
	0120	
004200 005000 A 1204 NOP 004201 000000 A 1205 MC22 ENTR 004202 100745 A 1206 EXC 0700+MPM DISABLE MP 004203 010010 A 1207 LDA 010 CHECK IF PROTECTED CORE MODIFIED. 01	0120 0120 0120 0120 0120	5 6 7
	0120	9
004211 004205 A	0121	
004212 050010 A 1211 STA 010 004213 002000 A 1212 CALL ERRR 004214 005035 A	0121	
	0121	3
004220 002157 A	0121	
004222 004230 A	0121	
004224 121102 A 1217 ADD N100 004225 051644 A 1218 STA ERRC	0121	7 8
	0122	0
004232 011644 A 1221 LDA ERRC 004233 151103 A 1222 ANA C100 004234 051644 A 1223 STA ' ERRC 01 004235 041644 A 1224 INR ERRC TEST 22 ** 01 004236 006010 A 1225 LDAI ML22	0122 0122 0122 0122	2 3 4
00 40 41 006010 A 1227 LDAI MC23 01	0122	
	0122	

PAGE 20	MAINTAIN	III	,	MEPROT		
004251 002000 A		EXC	0600+MPM TX22	ENABLE MP EXECUTE 2 WORD WRITE TO PA FROM UPA		1 01230 1 01231
004252 002161 A 004253 005000 A 004254 000000 A	1533 WC53	NOP ENTR			0	1 0 1
004255 100745 A 004256 002000 A 004257 005104 A	1235	CALL	0700+MPM ERLP	BISABLE MP	Û	1 01234
004260 041644 A 004261 006010 A 004262 004277 A		INR LDAI	ML23	TEST 23 ***	0	1 01236
004263 051645 A 004264 006010 A 004265 004302 A	1239	LDAI	MC24	LOOP ADDR.		1 01238
004266 051646 A 004267 002000 A 004270 001133 A	1241	CALL	CONT ILAS, WRT, ERRR	CONTINUE ADDR.		1 01240
004271 000004 A 004272 005035 A 004273 002000 A	The control of the co	CALL	ILAS, HALT, MC24	4 RETURN THROUGH HALT ERROR	. 0	1 01242
004274 001133 A 004275 000001 A 004276 004302 A		FUC	0.00.454	FNAN F NA	i.c	
004300 002000 A 004301 002166 A	E   E   21   25   25   25   25   25   25   25	CALL	0600+MPM TX23	EXECUTE 1 HORD WRITE TO UPA (NO INTERRUPT	r) 0	1 01243
004302 000000 A 004303 100745 A 004304 002000 A 004305 005104 A	1246 1247	ENTR EXC CALL	0700+MPM ERLP	DISABLE MP	0	1 01245 1 01246 1 01247
004306 041644 A 004307 006010 A 004310 004321 A	1248 1249	INR LDAI	ERRC ML24	TEST 24 **		1 01248
004311 051645 A 004312 006010 A 004313 004324 A	1250 1251	STA LDAI	MC25	LOOP ADDRESS		1 01250
004314 051646 A 004315 002000 A 004316 001133 A 004317 000001 A	1252 1253	STA	CONT ILAS, HALT, MC2	CONTINUE ADDRESS S RETURN THROUGH HALT ERROR		1 01252 1 01253
004320 004324 A 004321 100645 A 004322 002000 A 004323 002176 A	1255	EXC	0600+MPM TX24	ENABLE MP EXECUTE 2 WORD WRITE TO UPA (NO INTERRUPT		1 01254 1 01255
004324 0000000 A 004325 100745 A 004326 002000 A	1256 MC25 1257 1258	ENTR EXC CALL	0700+MPM ERLP	DISABLE MP	0	1 01256 1 01257 1 01
004327 005104 A 004330 041644 A 004331 006010 A	1259 1260	INR LDAI	ERRC ML25	TEST 25 *** 25 ***	4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4	1 01259
004332 004347 A 004333 051645 A 004334 006010 A	1261 1262	STA LDAI	MC26	LOOP ADDR.		1 01261
004335 004353 A 004336 051646 A 004337 002000 A 004340 001133 A	1263 1264	STA	CONT ILAS, HALT, ERRE	[20] - [		11 01263
004341 000001 A 004342 005035 A 004343 002000 A 004344 001133 A 004345 000004 A	1265	CALL	ILAS, WRT, MC26	RETURN THROUGH HALT ERROR	0	1 01265
004346 004353 A 004347 100645 A 004350 002000 A	1267	EXC CALL	0600+MPM TX25	ENABLE MP EXECUTE TEST		1 01266
004351 002207 A 004352 005000 A 004353 000000 A 004354 100745 A 004355 002000 A	1268 1269 MC26 1270	NDP ENTR EXC CALL	0700+MPM ERLP	DISABLE MP	0	1 01268 1 01269 1 01270 1 01271
004356 005104 A 004357 041644 A 004360 006010 A		INR LDAI	ERRC ML26	TEST 26 *** 26 ***		1 01272
004361 004376 A 004362 051645 A 004363 006010 A	1275	STA LDAI	LOOP MC27	LBOP ADDR.		01274 01275
004364 004401 A 004365 051646 A 004366 002000 A 004367 001133 A	1276	STA CALL	CONT ILAS, WRT, ERRR	CONTINUE ADDR.		0 1 2 7 6 0 1 2 7 7
004370 000004 A 004371 005035 A 004372 002000 A 004373 001133 A 004374 000001 A	1278	CALL	ILAS, HALT, MC22	7	0	1 01278
004375 004401 A 004376 100645 A 004377 002000 A 004400 002216 A	1280	EXC CALL	0600+MPM TX26	ENABLE MP EXECUTE TEST		1 01279
004401 0000000 A 004402 100745 A 004403 002000 A	1281 MC27 1282	ENTR EXC CALL	0700+MPM ERLP	DISABLE MP	0	1 01 1 01202 1 01283
004404 005104 A 004405 041644 A 004406 006010 A	회에 문제의 원래의 경기를 받는 것이 되었다.	INR LDAI	ERRC ML27	TEST 27 *** 27 ***		1 01284

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004407 004424 A 004410 051645 A 004411 006010 A	1286	STA LDAI	LDDP MG30-1	LOUP ADDR.		01286 01287
0 '12 004443 A 0 -13 051646 A 004414 002000 A	1288	STA	CONT ILAS, HALT, ERRI	CONTI. ADDR.	0 1	01288
004415 001133 A 004416 000001 A		CHLL	ILHSTHEITERK			01007
004417 005035 A 004420 002000 A 004421 001133 A	1290	CALL	ILAS, WRTO, MC3	RETURN FROM INTERRUPT	0 1	01290
004422 000100 A		FUE	0.600.808	PNARLE NE		
004424 100645 A 004425 002000 A 004426 002227 A	1292	CALL	060 <b>0+MPM</b> TX27	ENABLE MP EXECUTE TEST		01291
004427 000000 A 004430 100745 A 004431 006017 A	1294	ENTR EXC	0700+MPM	DISABLE MP	0 1	01293 01294
004432 004427 A 004433 006140 A	100000 00000	LDAE	03000 MC30	CHECK IF INTERRUPT ADDRESS CORRECT	01	01295 01296
	1297	JAZ	MG30		0 1	01297
004436 004444 A 004437 011644 A 004440 121102 A	1298	LDA	ERRC N100		0 1 0 1	01298 01299
004442 002000 A	1300 1301	STA CALL	ERRC		0 1 0 1	01300 01301
004443 005035 A 004444 002000 A 004445 005104 A	1302 MG30	CALL	ERLP		0 1	01302
004446 011644 A 004447 151103 A	1303 1304	L D A A N A	ERRC C100		0 1	01303 01304
004450 051644 A 004451 041644 A 004452 006010 A	1306	STA INR LDAI	ERRC ERRC ML30	TEST 30 **	01	01305 01306 01307
004453 004470 A 004454 051645 A	1308	STA	LOOP	LOOP ADDR.	0 1	01308
004455 006010 A 004456 004475 A 004457 051646 A	2 44 56	LDAI Sta	MC31 CDNT	CONTINUE ADDR.		01309
004460 002000 A 004461 001133 A	1311	CALL	ILAS, HRTO, ERR			01311
004462 000100 A 004463 005035 A 0-464 002000 A		CALL	ILAS, JPM, MC31	RETURN FROM INTERRUPT	0.1	01312
0 :466 000010 A		OALL	ILHS, SPIN, NOSI	KETOKII FRUIT INTERKOFT	0.1	01012
004467 004475 A 004470 100645 A 004471 002000 A	1313 ML30	EXC	0600+MPM TX30	ENABLE MP EXECUTE TEST		01313 01314
004472 002236 A	2 1343	CALL	ERRR	ENECOTE TEST		01315
004474 005035 A 004475 000000 A 004476 100745 A	1316 MC31	ENTR	0.700+MDM	DICABLE MO		01316
004477 002000 A 004500 005104 A	1318	CALL	0700+MPM ERLP	DISABLE MP	0 1	01317
004501 041644 A 004502 006010 A 004503 004514 A	1320	INR LDA <b>I</b>	ERRC ML31	TEST 31 ***		01319 01320
004504 051645 A 004505 006010 A	1321 1322	STALDAI	MC35	LOOP ADDR.		01321 01322
004506 004522 A 004507 051646 A 004510 002000 A	1323	STA	CONT ILAS, JPM, MC32	CONTINUE ADDR RETURN FROM INTERRUPT		01323 01324
004511 001133 A		w 11 to to	LEGOTOFITIOSE	ACTORD TRUE THECKNOTT	0.1	VIOLT
004513 004522 A 004514 100645 A 004515 002000 A	1325 ML31	EXC	0600+MPM TX31	ENABLE MP EXECUTE TEST		01325 01326
004516 002241 A 004517 005000 A	1327	NOP			0 1	01327
004520 002000 A 004521 005035 A 004522 000000 A		CALL Entr	ERRR			01328 01329
004523 100745 A 004524 002000 A	1330	CALL	0700+MPM ERLP	DISABLE MP	0 1	01330
004525 005104 A 004526 006012 A 004527 004512 A	1332	LDAE	MC38-3	CHECK IF JUMP AND MARK FROM UPA TO PA	0 1	01332
004530 006140 A 004531 005000 A	1333	ZABI	0500 <b>0</b>	CHANGED MARK ADDRESS.	0 1	01333
004532 001010 A 004533 004542 A	EN ==4443	JAZ	*+3			01334
004534 006017 A 004535 004531 A 004536 006057 A		LDAE	*-3 MC32-3			01335 01336
0 337 004517 A	1337	CALL	ERRR			01337
004541 005035 A 004542 041644 A 004543 006010 A	1338	INR LDÁI	ERRC ML32	TEST 35 **		01338
004544 004561 A 004545 051645 A		STA	LOOP	LOOP ADDR.		01339 01340

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PAGE 22	MAINTAIN	111	•	1EPROT		
004546 006010 A 004547 004567 A	1341	LDAI	MC33			01 01341
004550 051646 A	1342 1343	STA	CONT ILAS, JPM, ERRR	CONTINUE ADDR.		01 01342
004556 001133 A 004557 000200 A	1344	CALL	ILAS, JPMO, MC33	RETURN FROM INTERRUPT		01 01344
	13 <b>45 ML32</b> 13 <b>46</b>	EXC LDAI	0600+MPM ERRR	ENABLE MP EXECUTE TEST		01 01345 01 01346
004564 053000 A	13 <b>47</b> 1348	STA	03000 TX32	SET UP 1ST PA HITH ERROR SUB.	LDC.	01 01347 01 01348
. 그러나 맛이 있을 때로 어떤 그림들이 그렇게 있는 것이 했던 프라그램이 없어요? 그 그렇지?		ENTR EXC LDAI	070 <b>0+MPM</b> 0200 <b>0</b>	BISABLE MP RESTORE JMPM INST.		01 01349 01 01350 01 01351
004573 053000 A 004574 002000 A		STA Call	03000 ERLP			01 01352 01 01353
004575 005104 A 004576 041644 A 004577 006010 A		INR LDAI	ERRC ML33	TEST 33	** 33 **	01 01354 01 01355
004600 004615 A 004601 051645 A 004602 006010 A		STA LDAI	L00P MC34	LDOP ADDR.		01 01356 01 01357
004603 004623 A 004604 051646 A 004605 002000 A 004606 001133 A		STA	CONT ILAS, JPMO, ERRE	CBNTINUE ADDR.		01 01358 01 01359
004607 000200 A 004610 005035 A 004611 002000 A 004612 001133 A 004613 000010 A	1360	CALL	ILAS, JPM, MC34	RETURN FROM INTERRUPT		01 01360
004616 002000 A		EXC CALL	060 <b>0+MPM</b> Tx33	ENABLE MP EXECUTE TEST		01 01361 01 01362
	1363 1364	NOP CALL	ERR <b>R</b>			01 01363 01 01364
004624 100745 A 004625 002000 A	1365 MC34 1366 1367	ENTR EXC CALL	0700+ <b>MPM</b> ERLP	DISABLE MP	2.6	01 013 01 017 01 017
004626 005104 A 004627 041644 A 004630 006010 A		INR LDAI	ERRC ML34	TEST 34	** 34 **	01 01368 01 01369
004631 004642 A 004632 051645 A 004633 006010 A		STA LDAI	L00P MC3 <b>5</b>	LOOP ADDR.	•	01 01370 01 01371
004634 004650 A 004635 051646 A 004636 002000 A 004637 001133 A 004640 000010 A		STA CALL	CONT ILAS, JPM, MC35	CONTINUE ADDR.		01 01372 01 01373
004643 002 <b>00</b> 0 A	1374 ML34 1375	EXC CALL	060 <b>0+MPM</b> TX3 <b>4</b>	ENABLE MP EXECUTE TEST		01 01374 01 01375
004646 002000 A	1376 1377	NOP CALL	ERRR			01 01376 01 01377
004652 002000 A	1379	ENTR EXC CALL	0700+MPM ERLP	DISABLE MP		01 01378 01 01379 01 01380
	1381 * BYP 1382	ASS TEST	35 IF 620/F L	TITHOUT OPTIONAL INST. PRESENT	E4	01 01381 01 01382
004655 001010 A 004656 004661 A 004657 001000 A	1383	JAZ JMP	*+4 MC36+1			01 01383 01 01384
5일 [25] [2] 전시 [2] 하시면서 가게 된 것들고 그 모면서 하였다고 있는데 모든 경기 작업을 하는데 하는데 가지 않는다.	1385 1386	INR LDAI	ERRC ML35	TEST 35	** 35 **	01 01385 01 01386
004665 006010 A	1387 1388	STA LDAI	MC36	LODP ADDR.		01 01387 01 01388
[18일 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1389 1390	STA CALL	CONT ILAS, JPM, MC36	CONTINUE ADDR.		01 01389 01 01390
004673 004702 A 004674 100645 A 004675 002000 A 004676 002262 A		EXC CALL	0600+MPM TX35	ENABLE MP EXECUTE TEST		01 01391
004677 005000 A 004700 002000 A	1	NOP Call	ERRR			01 01323 01 01394
004701 005035 A 004702 000000 A 004703 002000 A	1395 MC36 1396	ENTR CALL	ERLP			01 01395 01 01396

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004704 005104 A	1397	LDAI	036	TEST 36 ** 36 4	<b>*</b> *	0 1	01397	
024706 000036 A 0 707 051644 A 0 710 006010 A	1398 1399	STA LDAI	ERRC ML36	LOUP ADDR.			01398 01399	
004711 004722 A 004712 051645 A 004713 006010 A		STA LDAI	L00P MG37-1	CONTINUE ADDRESS		0 1	01400 01401	
004714 004751 A 004715 051646 A 004716 002000 A 004717 001133 A 004720 000010 A		STA CALL	CONT ILAS, JPM, MC37				01402 01403	
004721 004727 A 004722 100645 A	1404 ML36 1405	EXC CALL	0600+MPM TX36	ENABLE MP Execute test			01404 01405	
12 - 12 - 12 - 12 - 12 - 12 - 12 - 12 -	1406	CALL	ERRR			0 1	01406	
004727 000000 A :		ENTR LDAE	MC37				01407 01408	
004731 004727 A 004732 006140 A 004733 004725 A	1409	SUBI	ML36+3			0 1	01409	
004734 001010 A :	1410	JAZ	<b>₩</b> + 4			0 1	01410	
004736 002000 A :	1411	CALL	ERRR	(**)		0 1	01411	
004740 011644 A 004741 121102 A 004742 051644 A 004743 102545 A 004744 006140 A 004745 002776 A	1413 1414 1415	LDA ADB STA CIA SUBI	ERRC N100 ERRC MPM 02776	INPUT INSTRUCTION REGISTER.		0 1 0 1 0 1	01412 01413 01414 01415 01416	
004746 001010 A 1	1417	JAZ	MG37			0 1	01417	
004750 002000 A : 004751 005035 A	1418	CALL	ERRR			0 1	01418	
004752 100745 A : 004753 002000 A : 004754 005104 A	20 PAGE (C.C. 1900)   10 PAGE (C.C. 1900)	CALL	0700+MPM ERLP	DISABLE MP			01419 01420	
004755 014144 A 004756 001010 A 004757 004762 A	1422	PASS TEST LDA Jaz	T 37 IF 620/F OPTT *+4	WITHOUT OPTIONAL INST. PRESENT CHECK IF OPTIONAL INST. PRESENT		0 1	01421 01422 01423	
0 760 001000 A	1424	JMP	ENDT			0 1	01424	
06-762 006010 A :	100 C	LDAI	037	TEST 37 ** 37	**	0 1	01425	
004764 051644 A : 004765 006010 A : 004766 004777 A	1427	STA LDAI	ML37	LOOP ADDR.			01426 01427	
004767 051645 A : 004770 006010 A : 004771 005002 A		STA LDAI	L00P MC38				01428 01429	
004772 051646 A 004773 002000 A 004774 001133 A 004775 000010 A		STA	CONT ILAS, JPM, MC38	CONTINUE ADDRESS			01430 01431	
004776 005002 A 004777 100645 A 005000 002000 A 005001 002276 A	1432 ML37 1433	EXC	060 <b>0+MPM</b> Tx37	ENABLE MP Execute test compair instruction			01432 01433	
005002 000000 A 005003 100745 A 005004 002000 A 005005 005104 A	1435	ENT <b>R</b> EXC CALL	0700+MPM ERLP	DISABLE MP		0 1	01434 01435 01436	
005006 011634 A 005007 005311 A	4.774, 1.174, 1.177, 1.171, 1.171, 1.171, 1.171, 1.171, 1.171, 1.171, 1.171, 1.171, 1.171, 1.171, 1.171, 1.171	L D A D A R	CYCL	CHECK IF CYCLE MODE			01437 01438	
005010 001004 A :	1439	JAN	MPT3	CONTINUAL MODE			01439	
005012 031104 A : 005013 001040 A : 005014 005017 A		L D X J X Z	MDDE *+4		D D		01440 01441	
005015 001000 A :	1442	JMP	MPT3		В	0 1	01442	
005017 001010 A :		JAZ	*+5	CYCLE COMPLETED			01443	
005021 051634 A : 005022 001000 A : 005023 003207 A	#170 C. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1.	AT 2 JMP	MPT3	CYCLES NOT COMPLETED			$01444 \\ 01445$	
005024 010442 A : 005025 001010 A : 005026 003136 A		LDA JAZ	SCON MPT1	CHECK IF CONSOLE MODE	D		01446 01447	
005027 006030 A :	1448	LDXI	MSG7	WRITE (MP TEST COMPLETE)		0 1	01448	
005031 002000 A 3		CALL#	מדטם			0 1	01449	
0 133 001000 A : 0 /34 003136 A		JMP	MPT1				01450	
1	1452 *			****************************			01451 01452	
	1453 * 1454 * 1455 *****			R INSTRUCTION INTERRUPT ADDRESS TEST	3	E 0 1	01453	

PAGE 24	MAINTAIN III		MEPROT	
005035 000000 A 005036 100745 A 005037 011645 A 005040 054015 A	1458 LDA 1459 STA	0700+MPM LDDP ERR5+5 CDNT	DISABLE MP SET UP SSWT CALLING SEQUENCE LOOP ADDRESS	01 01456 01 01457 01 01458 01 0: 3
005042 005111 A 005043 054014 A	1461 IAR 1462 STA	ERR6+1	CONTINUE ADDRESS	01 01461 01 01462
005045 064005 A		ERRC ERR5+2	ERROR CODE	01 01463 01 01464
005046 005001 A 005047 006037 A 005050 005035 A	1465 TZA 1466 LDXE	ERRR		01 01465 01 01466
005051 002000 A 005052 100421 A 005053 000000 A 005054 105061 A 005055 003136 A	1467 ERRS CALL	* SSHT, 0, (ERMS	)*,MPT1,0	01 01467
005056 000000 A 005057 001000 A 005060 000000 A	1468 ERR6 JMP	0	CONTINUE	01 01468
003000 000000 A		ROR SUB. WRITE M	ESSAGE ROUTINE	#01 01469 #01 01470
005061 000000 A 005062 006030 A				#01 01471 01 01472 01 01473
005063 005075 A	1474 CALL	æ .ourb	WRITE (ERROR TYPE = )	01 01474
005065 100403 A 005066 011644 A 005067 002000 A	그렇다 전계에 맞게 되었다.	ERRC W DUTE	WRITE ERROR TYPE	01 01475 01 01476
005070 100404 A	1477 CALL	* DUTC	CR/LF	01 01477
005072 100402 A 005073 001000 A 005074 105061 A	1478 JMP*	ERMS		01 01478
005075 142722 A 005076 151317 A	1479 MSG6 DATA	ERROR TYPE	=',0	01 01479
005077 151240 A 005100 152331 A 005101 150305 A 005102 120275 A 005103 000000 A 005104 000000 A		0.0071.0007.00 (0.000)	CHECK IE EDDAB LARDING	01 01480 01 01481
005105 010440 A 005106 001010 A 005107 005112 A	1481 LDA 1482 JAZ	\$FLG *+4	CHECK IF ERROR LOOPING	01 01482
005110 002000 A 005111 005035 A				01 01
005112 001400 A 005113 003136 A	5 144544	801 5E45		01 01484
005114 011233 A 005115 001010 A 005116 105104 A	1485 LDA 1486 JAZ*	ERLP		01 01485 01 01486
005117 002000 A 005120 001204 A	1487 CALL	CKSE		01 01487
그는 그는 그는 그 그 그 그를 가는 그는 그를 가는 것이 되었다. 그를 가는 것이 되었다.	1488 TEST DATA 1489 OPTT DATA 1490 OPTX DATA 1491 END	0	CPU/MP TYPE: 0=620/F 1=V73	01 01488 01 01489 01 01490 01 01491
SYMBOLS  000442 A \$CON  000441 A \$MEM  001204 A CKSE  001613 A EMSG  001562 A ERM1  005061 A ERR6  001342 A HBBT  001056 A IAR2  001114 A ILA2  000411 A INPB  000415 A INPF  000415 A MC05  001100 A LOCP  001643 A MC15  003521 A MC25  004650 A MC15  004650 A MC35  004475 A MC36  004324 A MC25  004475 A MC36  004475 A ML36  004561 A ML36  004561 A ML36  004561 A ML36	000424 A CLMP 001234 A END 001571 A ERRC 0001571 A ERRC 001644 A FIVE 001642 A HLDR 001642 A HLDR 001642 A HLDR 001642 A HLDR 0001264 A HC06 000416 A HC06 000375 A MC16 000375 A MC26 004353 A MC36 004702 A MC37 004444 A ML37 004444 A ML37	000770 A ERRR 001070 A FOUR 001070 A HLTF 001052 A HLTP 001133 A HNPD 001133 A HNPD 000413 A HC03 001410 A LBBT 001440 A MC03 003447 A MC13 004063 A MC13 004063 A MC27 004727 A MC37 004727 A MC37 004727 A MC37 004752 A MC37	000422 A SLWE 001103 A C100 001634 A CYCL 005104 A ERRDG 005051 A ERRS 0005051 A ERRS 000001 A HALTR 001105 A ILA1 001230 A HALTR 001105 A ILA1 000410 A INPA 000414 A INPE 003473 A MC04 003473 A MC04 003473 A MC14 004106 A MC20 004302 A MC24 0044023 A MC38 004151 A MG21 004040 A ML05 003755 A ML01 004040 A ML15 0044623 A MC14 0044623 A MC38 004151 A MG21 0033703 A ML11 004040 A ML15 004175 A ML21 004040 A ML25 004574 A ML35 004574 A ML35 004574 A ML35	

003412	A	MPC2	003417	Ĥ	MPC3	003175	A	MPCM	000045	A	MPM
003020	A	MPTO	003136	A	MPT1	003162	A	MPT2	003207	A	MPT3
003264	A	MPT4	003321	A	MPT5	003323	A	MPT6	003345	A	MPT7
7 305	A	MPTS	003016	A	MPTS	003010	A	MPTT	003171	A	MPZZ
t 456	A	MSG1	001472	A	MSG2	001477	A	MSG3	001510	A	MSG4
002313	A	MSG5	005075	A	MSGE	002330	A	MSG7	002342	A	MSG8
002367	A	MSG9	001635	A	MSKO	001636	A	MSK1	001637	A	MSKE
001640	A	MSK3	001102	A	N100	001075	A	N371	001074	A	N374
001022	A	N377	001073	A	N402	001076	A	N424	001065	A	DHE
005122	A	DPTT	005123	A	DPTX	000400	A		000401	A	DUTB
000402	A	DUTC	000403	A	DUTD	000404	A	DUTE	000405	A	DUTF
000406	A	DUTG	000407	A	DUTH	000020	A		001147	Ä	RES1
001153	A	RESR	001373	A	RHBB	001440	A		001331	A	RMB1
001322	A	RMBL	001140	A	SAVI	001156	A	SAVA	001144	Ä	SAVR
001161	A	SET1	001201	A	SETM	001072	A	XIZ	001064	A	TXIZ
001257	A	SSMI	001270	A	SZMS	001250	A		000421	A	THEE
001063	A	SVEN	001537	A	SYLB	000420	A	TDLY	005121	A	TEST
001067	A	THRE	000417	A	TUUT	000500	A	TSTI	000502	A	TSTA
000566	A	TSTB	000630	A	TSTC	000772	A		000740	A	TSTU
000711	А	TSTV	000670	A	TSTW	000647	A	TSTX	000605	A	TSTY
000520	Ä	TSTZ	001066	A	TWO	002000	A	TX01	002004	Ä	TXOZ
002011	A	TXO3	002016	A	. 프라마 (1200 HELD HOUSE)	002024	A		002032	A	TX06
002040	100	TXOZ	002046	Ä	TX10	002060	10.00	TX11	002072	2.00	TX12
002102		TX13		A				TX15		A	
002126		TX17	002135					TX21	002161	A	TX22
002166		TXZZ	002176					TXZS	002216		TX26
002227			002236			30 <u>2</u> 46021 1227 1226 1207 1206	A		002244		TX32
002252			002256			002262	30099		002266		TX36
002276			000004		URT	000100	A	WRTD	001633		RIMS
			MBLY COM			000100	п	AILL	001000	-	HIIIO
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